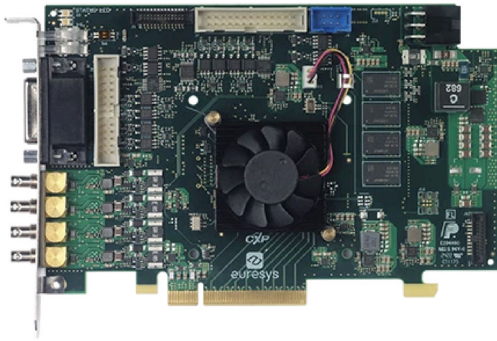


5/22/2024

Datasheet

Coaxlink Quad CXP-12 Value

Four-connection CoaXPress CXP-12 frame grabber



- Four CoaXPress CXP-12 connections: 5,000 MB/s camera bandwidth
- PCIe 3.0 (Gen 3) x8 bus: 6,700 MB/s bus bandwidth
- Feature-rich set of 20 digital I/O lines
- Extensive camera control functions
- Memento Event Logging Tool

Main benefits



PCIe 3.0 (Gen 3) x8 bus

- 7,800 MB/s peak bus bandwidth
 - 6,700 MB/s sustained bus bandwidth
-



Compatible with eGrabber

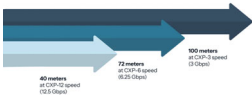
- [eGrabber Studio](#): eGrabber's new interactive evaluation and demonstration application
 - [GenICam Browser](#): An application giving access to the GenICam features exposed by the GenTL Producer(s)
 - GenTL Console: A command-line tool giving access to the functions and commands exposed by the Euresys GenTL Producer
-



Compliant with GenICam

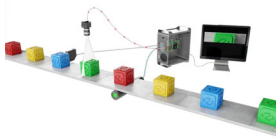
Including support for:

- GenApi
 - The Standard Feature Naming Convention (SFNC)
 - GenTL
-



Long cable support

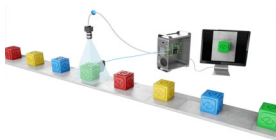
- 40 meters at CXP-12 speed (12.5 Gbps)
- 72 meters at CXP-6 speed (6.25 Gbps)
- 100 meters at CXP-3 speed (3 Gbps)



Line-scan triggering capabilities

Euresys' frame grabbers offer many capabilities to synchronize line-scan or 1D cameras, sensors and lighting controllers. Frame grabbers can control the camera scanning rate based on the signals received from a motion encoder.

They support continuous web scanning (to inspect infinite, continuously moving surfaces without losing a single line) and discrete object scanning (to acquire the image of objects moving in front of the camera).



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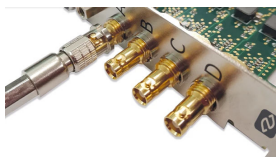
Power over CoaXPress

- Power over CoaXPress : Feed your camera up to 17 W per channel under 24 VDC with automatic device detection, measurement and overload protection.
- Total and per-channel voltage and current measurement is possible, allowing validation and performance deviation monitoring.



Use standard coaxial cables

- A single inexpensive cable for data transfer, camera control, trigger and power supply
- Top reliability and flexibility, performs in the harshest environments



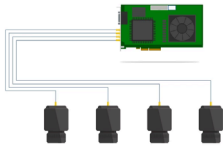
Micro-BNC (HD-BNC™) connectors for reliable connection

- Trusted push and turn, bayonet-style positive lock
- Allows for quick and easy connects and disconnects



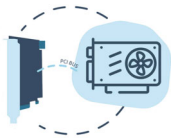
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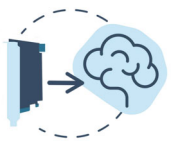
Connect up to 4 cameras to a single Coaxlink card

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Direct GPU transfer

- Sample programs for AMD DirectGMA and NVIDIA (CUDA) available.
- Direct GPU transfer eliminates unnecessary system memory copies, lowers CPU overhead, and reduces latency, resulting in significant performance improvements in data transfer times for applications.
- Direct capture of image data to GPU memory is available using AMD's DirectGMA. Compatible with AMD FirePro W5x00 and above and all AMD FirePro S series products.



High-performance DMA (Direct Memory Access)

- Direct transfer into user-allocated memory
- Hardware scatter-gather support



C2C-Link camera synchronization

Allows to accurately synchronize multiple area-scan or line-scan cameras connected

- to the same card
- to different cards in the same PC
- to different cards in different PCs

ARM **macOS**



LINUX



Windows

Windows, Linux and macOS drivers available

Including support for Intel 64-bit platforms as well as ARM 64-bit platforms.

Other benefits

Line-scan Metadata insertion

When activated, this feature records metadata beside image data. Line metadata are captured every acquired image line. Buffer metadata are only captured when the first image line of a buffer is acquired. The metadata are composed with a configurable set of general purpose event counters, quadrature encoder position counters and/or I/O line status. This feature allows line-scan applications to correlate image data with system events including motion encoder positions.

Flexible line-scan camera operation with the rate converter

- The rate converter is a smart, programmable frequency multiplier/divider.
- Used with motion encoders and line-scan cameras, it allows the user to choose the aspect ratio of the pixels in the image.
- It provides a way to calibrate the acquisition chain to easily reach square (1:1 aspect ratio) pixels.

Specifications

Mechanical

Form factor

PCI Express card

Format

Standard profile, half length, 8-lane PCI Express card

Cooling method

Air cooling, fan-cooled heatsink

Mounting

For insertion in a standard height, 8-lane or higher, PCI Express card slot

Connectors

'A B C D' on card bracket:

4 x Micro-BNC 75 Ohms coaxial receptacles

CoaXPress Host Interface

'EXTERNAL I/O' on card bracket:

26-pin 3-row high-density D-Sub female socket with UNC4-40 jack socket screws

I/O lines and I/O power output

'INTERNAL I/O 1' on printed circuit board:

26-pin 2-row 0.1" pitch pin header with shrouding

I/O lines and I/O power output

'INTERNAL I/O 2' on printed circuit board:

26-pin 2-row 0.1" pitch pin header with shrouding

I/O lines and I/O power output

'I/O EXTENSION' on printed circuit board:

26-pin 2-row 0.05" pitch pin header with shrouding

I/O extension cable socket

'C2C-LINK' on printed circuit board:

6-pin 2-row 0.1" pitch pin header with shrouding

Card-to-card link

'AUXILIARY POWER INPUT' on printed circuit board:

6-pin PEG power socket

12 V DC power input for PoCXP and I/O power output

LED indicators

'A', 'B', 'C', 'D' on bracket:

Bi-color red/green LEDs

CoaXPress Host connector indicator

'FPGA STATUS LAMP' on PCB:

Bi-color red/green LED

FPGA status indicator

'BOARD STATUS LAMP' on PCB:

Bi-color red/green LED

Board status indicator

Switches

'RECOVERY' on PCB:

3-pin 1-row 0.1" header or 2-way DIP switch

Firmware emergency recovery

Dimensions

PCB L x H: 167.65 mm x 111.15 mm [6.6 in x 4.38 in]

Weight

Net weight: 187 g [6.6 oz]

Gross weight: 287 g [10.1 oz]

Host bus

Standard

PCI Express 3.0

Link width

8 lanes

1 lane, 2 lanes or 4 lanes with reduced performance

Link speed

8.0 GT/s (PCIe 3.0)

5.0 GT/s (PCIe 2.0) with reduced performance

Maximum payload size

512 bytes

DMA

32- and 64-bit

Peak delivery bandwidth

7,800 MB/s

Effective (sustained) delivery bandwidth

6,700 MB/s (Host PC motherboard dependent)

Power consumption

Typ. 16.7 W (3.3 W @ +3.3V, 13.4 W @ +12V), excluding camera and I/O power output

Camera / video inputs

Camera interface standard

CoaXPress

Interface standard(s)

CoaXPress 1.0, 1.1, 1.1.1, 2.0 and 2.1

Maximum link speed

CXP-12

Maximum link width

4 connections

Camera powering

PoCXP

Connectors

Four micro-BNC 75 Ohms (also known as HD-BNC™) CXP-12

Status LEDs

One CoaXPress Host connection status LED per connection

Number of cameras

Area-scan cameras:

- One 1- or 2- or 4-connection camera
- Two 1- or 2-connection cameras
- Four 1-connection cameras

Line-scan cameras:

- One 1- or 2- or 4-connection camera
- Two 1- or 2-connection cameras
- Four 1-connection cameras

Maximum number of cameras

4

Line-scan cameras supported

Yes

Maximum aggregated camera data transfer rate

50 Gbps (5,000 MB/s)

Supported CXP down-connection speeds

1.25 Gbps (CXP-1), 2.5 Gbps (CXP-2), 3.125 Gbps (CXP-3), 5 Gbps (CXP-5), 6.25 Gbps (CXP-6), 10.0 Gbps (CXP-10), and 12.5 Gbps (CXP-12)

Supported CXP up-connection speeds

Low-speed 20.83... Mbps (CXP-1 to CXP-6)

Low-speed 41.66... Mbps (CXP-10, CXP-12)

Number of CXP data streams (per camera)

1 data stream per camera

Maximum CXP stream packet size

16,384 bytes

PoCXP (Power over CoaXPress)

PoCXP Safe Power:

- 17 W of 24V DC regulated power per CoaXPress connector

- PoCXP Device detection and automatic power-on

- Overload and short-circuit protections

On-board 12V to 24V DC/DC converter

A +12V power source must be connected to the AUXILIARY POWER INPUT connector using a 6-pin PEG cable

Camera types

Area-scan cameras:

- Grayscale and color (YCbCr, YUV, RGB and Bayer CFA)

- Single-tap (1X-1Y) progressive-scan

- Two-tap (1X-2YE) on two distinct data streams ('1-camera' firmware variant only)

Line-scan cameras and contact imaging sensors:

- Grayscale and color RGB

Camera pixel formats supported

Mono8, Mono10, Mono12, Mono14, Mono16

BayerXX8, BayerXX10, BayerXX12, BayerXX14, BayerXX16 where XX = GR, RG, GB, or BG

RGB8, RGB10, RGB12, RGB14, RGB16
RGBA8, RGBA10, RGBA12, RGBA14, RGBA16
YCbCr601_422_8, YCbCr601_422_10
YCbCr709_422_8, YCbCr709_422_10
YUV422_8, YUV422_10
Raw

Area-scan camera control

Trigger

Precise control of asynchronous reset cameras, with exposure control.
Support of camera exposure/readout overlap.
Support of external hardware trigger, with optional delay and trigger decimation.

Strobe

Accurate control of the strobe position for strobed light sources.
Support of early and late strobe pulses.

Line-scan camera control

Scan/page trigger

Precise control of start-of-scan and end-of-scan triggers.
Support of external hardware trigger, with optional delay.
Support of infinite acquisition, without missing line, for web inspection applications.

Line trigger

Support for quadrature motion encoders, with programmable noise filters, selection of acquisition direction and backward motion compensation.
Rate Converter tool for fine control of the pixel aspect ratio: Rate Conversion Ratio in the range 0.001 to 1000 with an accuracy better than 0.1%.
Rate Divider tool

Line strobe

Accurate control of the strobe position for strobed light sources.

On-board processing

On-board memory

4 GB

Image data stream processing

Unpacking of 10-/12-/14-bit to 16-bit with selectable justification to LSB or MSb

Optional swap of R and B components

Little endian conversion

Flat-field correction

Only available with the '1-camera' firmware variant

Input LUT (Lookup Table)

Monochrome 8-bit to 8-bit transformation

Monochrome 10-bit to 8-, 10- or 16-bit transformations

Monochrome 12-bit to 8-, 12- or 16-bit transformations

Bayer CFA to RGB decoder

'1-camera' firmware variant:

3x3 linear interpolation method

3x3 median-based interpolation method

Pixel binning

'1-camera' firmware variant:

2x2 and 4x4 binning windows

Bypass, sum and mean methods

Data stream statistics

Measurement of:

Frame rate (Area-scan only)

Line rate

Data rate

Configurable averaging interval

Event signaling and counting

The application software can be notified of the occurrence of various events:

Standard event: the EVENT_NEW_BUFFER event notifies the application of newly filled buffers

A large set of custom events

Custom events sources:

I/O Toolbox events

Camera and Illumination control events

CoaXPress data stream events

CoaXPress host interface events

Each custom event is associated with a 32-bit counter that counts the number of occurrences

The last three 32-bit context data words of the event context data can be configured with event-specific context data:

Event-specific data

State of all System I/O lines sampled at the event occurrence time

Value of any event counter

General Purpose Inputs and Outputs

Number of lines

20 I/O lines:

4 differential inputs (DIN)

4 singled-ended TTL inputs/outputs (TTLIO)

8 isolated inputs (IIN)

4 isolated outputs (IOUT)

NOTE: The number of I/O lines can be extended using I/O modules attached to the I/O EXTENSION connector.

Usage

Any I/O input lines can be used by any LIN tool of the I/O Toolbox

Selected pairs of I/O input lines can be used by any QDC tool of the I/O toolbox to decode A/B signals of a motion encoder

Electrical specifications

DIN: High-speed differential inputs, up to 5 MHz, compatible with ANSI/EIA/TIA-422/485 differential line drivers and complementary TTL drivers

TTLIO: High-speed 5V-compliant TTL inputs or LVTTTL outputs, compatible with totem-pole LVTTTL, TTL, 5V CMOS drivers or LVTTTL, TTL, 3V CMOS receivers

IIN: Isolated current-sense inputs with wide voltage input range up to 30V, signaling up to 200 kHz, individual galvanic isolation up to 250VDC and 170 VAC, compatible with totem-pole LVTTTL, TTL, 5V CMOS drivers, RS-422 differential line drivers, potential free contacts, solid-state relays and opto-couplers

IOUT: Isolated contact outputs compatible with 30V / 100mA loads

NOTE: IIN and IOUT lines provide a functional isolation grade for the circuit technical protection. It does not provide an isolation that can protect a human being from electrical shock!

Filter control

Glitch removal filter available on all System I/O input lines

Configurable filter delay:

Custom value

Fixed values for DIN and TTLIO lines: 50 ns, 100 ns, 200 ns, 500 ns, 1 μ s

Fixed values for IIN lines: 500 ns, 1 μ s, 2 μ s, 5 μ s, 10 μ s

Polarity control

Yes

Power output

Non-isolated, +12V, 1A, with electronic fuse protection

I/O Toolbox tools

The I/O Toolbox is a configurable interconnection of tools that generates events (usually triggers):

Line Input tool (LIN): edge detector delivering events on rising or falling edges of any selected input line.

Quadrature Decoder tool (QDC): a composite tool including:

- A quadrature edge detector delivering events on selected transitions of selected pairs of input lines.
- An optional backward motion compensator for clean line-scan image acquisition when the motion is unstable.
- A 32-bit up/down counter for delivering a position value.

Device Link Trigger tool (DLT): delivers an event on reception of a valid high-speed CoaXPress 2.0 connection trigger packet message from the remote device.

User Actions Scheduler tool (UAS): to delegate the execution of 'User Actions' at a scheduled time or encoder position. Possible user actions include setting low/high/toggle any bit of the User Output Register or generation of any User Events.

Delay tool (DEL): to delay up to 16 events from one or two I/O toolbox event sources, by a programmable time or number of motion encoder ticks (any QDC events).

Divider tool (DIV): to generate an event every nth input events from any I/O toolbox event source.

Multiplier/divider tool (MDV): to generate m events every d input events from any I/O toolbox event source.

The 'Input Tools' (LIN, QDC, DLT and UAS) can be further processed by the 'Event Tools' (DEL, DIV and MDV) to generate any of the following "trigger" events:

- The "cycle trigger" of the Camera and Illumination controller
- The "cycle sequence trigger" of the Camera and Illumination controller
- The "start-of-scan trigger" of the Acquisition Controller (line-scan only)
- The "end-of-scan trigger" of the Acquisition Controller (line-scan only)

I/O Toolbox composition

Determined by the selected firmware variant:

'1-camera': 8 LIN, 1 QDC, 2 DLT, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 2 C2C

'2-camera': 8 LIN, 1 QDC, 4 DLT, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 2 C2C

'4-camera': 8 LIN, 4 QDC, 8 DLT, 1 UAS, 2 DEL, 4 DIV, 4 MDV, 2 C2C

'1-camera, line-scan': 8 LIN, 1 QDC, 2 DLT, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 3 C2C

'2-camera, line-scan': 8 LIN, 2 QDC, 4 DLT, 1 UAS, 2 DEL, 2 DIV, 2 MDV, 3 C2C

'4-camera, line-scan': 8 LIN, 4 QDC, 8 DLT, 1 UAS, 4 DEL, 4 DIV, 4 MDV, 3 C2C

C2C-Link

Description

Accurate synchronization of the trigger and the start-of-exposure of multiple grabber-controlled area-scan cameras.

Accurate synchronization of the start-of-cycle, start-of-scan and end-of-scan of multiple grabber-controlled line-scan cameras.

Specification

C2C-Link synchronizes cameras connected to:

the same card

to different cards in the same PC (requires an accessory cable such as the "3303 C2C-Link Ribbon Cable" or a custom-made C2C-Link cable)

to different cards in different PCs (requires one "1636 InterPC C2C-Link Adapter" for each PC and one RJ 45 CAT 5 STP straight LAN cable for each adapter but the last one)

Maximum distance:

120 cm inside a PC

1200 m cumulated adapter to adapter cable length

Maximum trigger rate:

2.5 MHz for configurations using a single PC, or up to 10 PCs and 100 m total C2C-Link cable length

200 kHz for configurations up to 32 PCs and 1200m total C2C-Link cable length

Trigger propagation delay from master to slave devices:

Less than 10 ns for cameras on the same card or on different cards in the same PC

Less than 265 ns for cameras on different cards in different PCs (3 PCs and 40m total C2C-Link cable length)

Software

Host PC Operating System

Microsoft Windows 11, 10 for x86-64 (64-bit) processor architecture

Linux for x86-64 (64-bit) and AArch64 (64-bit) processor architectures

macOS for x86-64 (64-bit) and AArch64 (64-bit) processor architectures

APIs

EGrabber class, with C++ and .NET APIs: .NET assembly designed to be used with development environments

compatible with .NET frameworks version 4.0 or higher

GenICam GenTL producer libraries compatible with C/C++ compilers:

'x86_64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of x86-64 (64-bit) applications

'aarch64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of AArch64 (64-bit) applications

Memento supported

Yes

Environmental conditions

Operating ambient air temperature

0 °C to +55 °C / +32 °F to +131 °F

Operating ambient air humidity

10% to 90% RH non-condensing

Storage ambient air temperature

-20 °C to +70 °C/ -4 °F to +158 °F

Storage ambient air humidity

10% to 90% RH non-condensing

Certifications

EMC standards

European Council EMC Directive 2014/30/EU

United States FCC rule 47 CFR 15

EMC - Emission

EN 55032:2015 / CISPR 32:2012 Class B

FCC 47 Part 15 Class B

EMC - Immunity

EN 55024:2010 / CISPR 24:2010

EN 55035:2017 / CISPR 35:2016

EN 61000-4-2:2009

EN 61000-4-3:2006

EN 61000-4-4:2004

EN 61000-4-6:2014

KC Certification

Korean Radio Waves Act, Article 58-2, Clause 3

Flammability

PCB compliant with UL 94 V-0

RoHS

European Union Directive [2015/863](#) (ROHS3)

REACH

European Union Regulation [1907/2006](#)

WEEE

Must be disposed of separately from normal household waste and must be recycled according to local regulations

Ordering Information

Product code - Description

PC3623 Coaxlink Quad CXP-12 Value

Related products

PC1625 DB25F I/O Adapter Cable

PC1636 InterPC C2C-Link Adapter

PC3303 C2C-Link Ribbon Cable

PC3304 HD26F I/O Adapter Cable

PC3610 HD26F I/O Extension Module - TTL-RS422

PC3612 HD26F I/O Extension Module - TTL-CMOS5V-RS422

Offices

- Europe, Middle East & Africa
Euresys SA
Contact support : support.europe@euresys.com

Sensor to Image GmbH
Contact support : support.europe@euresys.com
- China
Euresys Shanghai Liaison Office
Contact support : support.china@euresys.com

Euresys Shenzhen Liaison Office
Contact support : support.china@euresys.com
- Japan
Euresys Japan K.K.
Contact support : support.japan@euresys.com
- South Korea
Euresys South Korea Liaison Office
Contact support : support.korea@euresys.com
- Asia (other countries)
Euresys Pte. Ltd.
Contact support : support.asia@euresys.com
- North, Central & South America
Euresys Inc.
Contact support : support.usa@euresys.com

TKH Vision Experience Center
Contact support : support.usa@euresys.com

5/22/2024

Datasheet

Coaxlink Quad CXP-12 DF

Four-connection CoaXPress CXP-12 frame grabber with Data Forwarding



- Four CoaXPress CXP-12 connections and four Data Forwarding outputs: 5,000 MB/s camera bandwidth
- PCIe 3.0 (Gen 3) x8 bus: 6,700 MB/s bus bandwidth
- Feature-rich set of 10 digital I/O lines
- Extensive camera control functions
- Memento Event Logging Tool

Main benefits



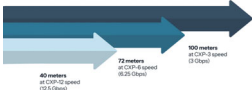
Acquire images from the fastest and highest resolution cameras

- Highest data acquisition rate in the industry
 - Up to 5,000 MB/s bandwidth from camera to host PC memory
-



PCIe 3.0 (Gen 3) x8 bus

- 7,800 MB/s peak bus bandwidth
 - 6,700 MB/s sustained bus bandwidth
-



Long cable support

- 40 meters at CXP-12 speed (12.5 Gbps)
 - 72 meters at CXP-6 speed (6.25 Gbps)
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Power over CoaXPress

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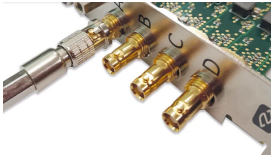
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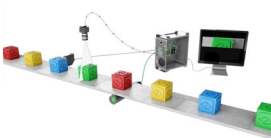
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- A single inexpensive cable for data transfer, camera control, trigger and power supply
- Top reliability and flexibility, performs in the harshest environments



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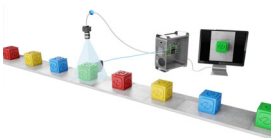
- Trusted push and turn, bayonet-style positive lock
- Allows for quick and easy connects and disconnects



Line-scan triggering capabilities

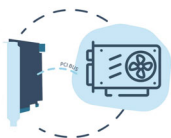
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- Direct transfer into user-allocated memory
- Hardware scatter-gather support



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- [GenICam](#) Browser: An application giving access to the GenICam features exposed by the GenTL Producer(s)
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ARM **macOS**



Windows, Linux and macOS drivers available

Including support for Intel 64-bit platforms as well as ARM 64-bit platforms.

Other benefits

General purpose I/O lines

- Compatible with a wide range of sensors and motion encoders.
 - High-speed differential inputs: Quadrature motion encoder support up to 5 MHz.
 - Isolated current-sense inputs: 5V, 12V, 24V signaling voltages accepted, up to 50 kHz, individual galvanic isolation up to 250VDC and 170VAC RMS.
 - Isolated contact outputs.
 - High-speed 5V-compliant TTL inputs/ LVTTTL outputs.
-

Flexible line-scan camera operation with the rate converter

- The rate converter is a smart, programmable frequency multiplier/divider.
- Used with motion encoders and line-scan cameras, it allows the user to choose the aspect ratio of the pixels in the image.
- It provides a way to calibrate the acquisition chain to easily reach square (1:1 aspect ratio) pixels.

Specifications

Mechanical

Form factor

PCI Express card

Format

Standard profile, half length, 8-lane PCI Express card

Cooling method

Air cooling, fan-cooled heatsink

Mounting

For insertion in a standard height, 8-lane or higher, PCI Express card slot

Connectors

'A B C D' on card bracket:

4 x Micro-BNC 75 Ohms coaxial receptacles
CoaXPress Host Interface

'FA FB FC FD' on card bracket:

4 x Micro-BNC 75 Ohms coaxial receptacles
CoaXPress Data Forwarding Interface

'INTERNAL I/O 1' on printed circuit board:

26-pin 2-row 0.1" pitch pin header with shrouding
I/O lines and I/O power output

'I/O EXTENSION' on printed circuit board:

26-pin 2-row 0.05" pitch pin header with shrouding
I/O extension cable socket

'C2C-LINK' on printed circuit board:

6-pin 2-row 0.1" pitch pin header with shrouding
Card-to-card link

'AUXILIARY POWER INPUT' on printed circuit board:

6-pin PEG power socket
12 V DC power input for PoCXP and I/O power output

LED indicators

'A', 'B', 'C', 'D' on bracket:

Bi-color red/green LEDs

CoaXPress Host connector indicator

'FPGA STATUS LAMP' on PCB:

Bi-color red/green LED

FPGA status indicator

'BOARD STATUS LAMP' on PCB:

Bi-color red/green LED

Board status indicator

Switches

'RECOVERY' on PCB:

2-way DIP switch

Firmware emergency recovery

Dimensions

PCB L x H: 167.65 mm x 111.15 mm [6.6 in x 4.38 in]

Weight

Net weight: 188 g [6.6 oz]

Gross weight: 286 g [10.1 oz]

Host bus

Standard

PCI Express 3.0

Link width

8 lanes

1 lane, 2 lanes or 4 lanes with reduced performance

Link speed

8.0 GT/s (PCIe 3.0)

5.0 GT/s (PCIe 2.0) with reduced performance

Maximum payload size

512 bytes

DMA

32- and 64-bit

Peak delivery bandwidth

7,800 MB/s

Effective (sustained) delivery bandwidth

6,700 MB/s (Host PC motherboard dependent)

Power consumption

Typ. 17.6 W (3.6 W @ +3.3V, 14 W @ +12V), excluding camera and I/O power output

Camera / video inputs

Camera interface standard

CoaXPress

Interface standard(s)

CoaXPress 1.0, 1.1, 1.1.1, 2.0 and 2.1

Maximum link speed

CXP-12

Maximum link width

4 connections

Camera powering

PoCXP

Connectors

Four micro-BNC 75 Ohms (also known as HD-BNC™) CXP-12

Status LEDs

One CoaXPress Host connection status LED per connection

Number of cameras

Area-scan cameras:

One 1- or 2- or 4-connection camera

Line-scan cameras:

One 1- or 2- or 4-connection camera

Maximum number of cameras

1

Line-scan cameras supported

Yes

Maximum aggregated camera data transfer rate

50 Gbps (5,000 MB/s)

Supported CXP down-connection speeds

1.25 Gbps (CXP-1), 2.5 Gbps (CXP-2), 3.125 Gbps (CXP-3), 5 Gbps (CXP-5), 6.25 Gbps (CXP-6), 10.0 Gbps (CXP-10), and 12.5 Gbps (CXP-12)

Supported CXP up-connection speeds

Low-speed 20.83... Mbps (CXP-1 to CXP-6)

Low-speed 41.66... Mbps (CXP-10, CXP-12)

Number of CXP data streams (per camera)

1 data stream per camera

Maximum CXP stream packet size

16,384 bytes

PoCXP (Power over CoaXPress)

PoCXP Safe Power:

- 17 W of 24V DC regulated power per CoaXPress connector

- PoCXP Device detection and automatic power-on

- Overload and short-circuit protections

On-board 12V to 24V DC/DC converter

A +12V power source must be connected to the AUXILIARY POWER INPUT connector using a 6-pin PEG cable

Camera types

Area-scan cameras:

- Grayscale and color (YCbCr, YUV, RGB and Bayer CFA)

- Single-tap (1X-1Y) progressive-scan

Line-scan cameras and contact imaging sensors:

- Grayscale and color RGB

Camera pixel formats supported

Mono8, Mono10, Mono12, Mono14, Mono16

BayerXX8, BayerXX10, BayerXX12, BayerXX14, BayerXX16 where XX = GR, RG, GB, or BG

RGB8, RGB10, RGB12, RGB14, RGB16

RGBA8, RGBA10, RGBA12, RGBA14, RGBA16

YCbCr601_422_8, YCbCr601_422_10

YCbCr709_422_8, YCbCr709_422_10

YUV422_8, YUV422_10

Raw

Area-scan camera control

Trigger

Precise control of asynchronous reset cameras, with exposure control.
Support of camera exposure/readout overlap.
Support of external hardware trigger, with optional delay and trigger decimation.

Strobe

Accurate control of the strobe position for strobed light sources.
Support of early and late strobe pulses.

Line-scan camera control

Scan/page trigger

Precise control of start-of-scan and end-of-scan triggers.
Support of external hardware trigger, with optional delay.
Support of infinite acquisition, without missing line, for web inspection applications.

Line trigger

Support for quadrature motion encoders, with programmable noise filters, selection of acquisition direction and backward motion compensation.
Rate Converter tool for fine control of the pixel aspect ratio: Rate Conversion Ratio in the range 0.001 to 1000 with an accuracy better than 0.1%.
Rate Divider tool

Line strobe

Accurate control of the strobe position for strobed light sources.

On-board processing

On-board memory

4 GB

Image data stream processing

Unpacking of 10-/12-/14-bit to 16-bit with selectable justification to LSb or MSb
Optional swap of R and B components
Little endian conversion

Flat-field correction

Only available with the '1-camera,line-scan' and '1-df-camera, line-scan' firmware variants

Input LUT (Lookup Table)

Monochrome 8-bit to 8-bit transformation

Monochrome 10-bit to 8-, 10- or 16-bit transformations

Monochrome 12-bit to 8-, 12- or 16-bit transformations

Bayer CFA to RGB decoder

Only on '1-camera' and '1-df-camera' firmware variants:

3x3 linear interpolation method (Method 1)

3x3 median-based interpolation method (Method 2)

5x5 gradient-based interpolation (Method 3)

Data stream statistics

Measurement of:

Frame rate (Area-scan only)

Line rate

Data rate

Configurable averaging interval

Event signaling and counting

The application software can be notified of the occurrence of various events:

Standard event: the EVENT_NEW_BUFFER event notifies the application of newly filled buffers

A large set of custom events

Custom events sources:

I/O Toolbox events

Camera and Illumination control events

CoaXPress data stream events

CoaXPress host interface events

Each custom event is associated with a 32-bit counter that counts the number of occurrences

The last three 32-bit context data words of the event context data can be configured with event-specific context data:

Event-specific data

State of all System I/O lines sampled at the event occurrence time

Value of any event counter

General Purpose Inputs and Outputs

Number of lines

10 I/O lines:

2 differential inputs (DIN)

2 singled-ended TTL inputs/outputs (TTLIO)

4 isolated inputs (IIN)

2 isolated outputs (IOUT)

Usage

Any I/O input lines can be used by any LIN tool of the I/O Toolbox

Selected pairs of I/O input lines can be used by any QDC tool of the I/O toolbox to decode A/B signals of a motion encoder

Electrical specifications

DIN: High-speed differential inputs, up to 5 MHz, compatible with ANSI/EIA/TIA-422/485 differential line drivers and complementary TTL drivers

TTLIO: High-speed 5V-compliant TTL inputs or LVTTTL outputs, compatible with totem-pole LVTTTL, TTL, 5V CMOS drivers or LVTTTL, TTL, 3V CMOS receivers

IIN: 200 kHz isolated current-sense input with wide voltage input range up to 30V, compatible with totem-pole (push-pull) HTL drivers, 5V TTL/RS-422 differential line drivers, 5V CMOS drivers, potential free contacts, solid-state relays and opto-couplers

IOUT: Isolated contact outputs compatible with 30V / 100mA loads

NOTE: IIN and IOUT lines provide a functional isolation grade for the circuit technical protection. It does not provide an isolation that can protect a human being from electrical shock!

Filter control

Glitch removal filter available on all System I/O input lines

Configurable filter delay:

Custom value

Fixed values for DIN and TTLIO lines: 50 ns, 100 ns, 200 ns, 500 ns, 1 μ s

Fixed values for IIN lines: 500 ns, 1 μ s, 2 μ s, 5 μ s, 10 μ s

Polarity control

Yes

Power output

Non-isolated, +12V, 1A, with electronic fuse protection

I/O Toolbox tools

The I/O Toolbox is a configurable interconnection of tools that generates events (usually triggers):

Line Input tool (LIN): edge detector delivering events on rising or falling edges of any selected input line.

Quadrature Decoder tool (QDC): a composite tool including:

A quadrature edge detector delivering events on selected transitions of selected pairs of input lines.

An optional backward motion compensator for clean line-scan image acquisition when the motion is unstable.

A 32-bit up/down counter for delivering a position value.

Device Link Trigger tool (DLT): delivers an event on reception of a valid high-speed CoaXPress 2.0 connection trigger packet message from the remote device.

User Actions Scheduler tool (UAS): to delegate the execution of 'User Actions' at a scheduled time or encoder position. Possible user actions include setting low/high/toggle any bit of the User Output Register or generation of

any User Events.

Delay tool (DEL): to delay up to 16 events from one or two I/O toolbox event sources, by a programmable time or number of motion encoder ticks (any QDC events).

Divider tool (DIV): to generate an event every nth input events from any I/O toolbox event source.

Multiplier/divider tool (MDV): to generate m events every d input events from any I/O toolbox event source.

The 'Input Tools' (LIN, QDC, DLT and UAS) can be further processed by the 'Event Tools' (DEL, DIV and MDV) to generate any of the following "trigger" events:

- The "cycle trigger" of the Camera and Illumination controller
- The "cycle sequence trigger" of the Camera and Illumination controller
- The "start-of-scan trigger" of the Acquisition Controller (line-scan only)
- The "end-of-scan trigger" of the Acquisition Controller (line-scan only)

I/O Toolbox composition

8 LIN, 1 QDC, 2 DLT, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 3 C2C

C2C-Link

Description

Accurate synchronization of the trigger and the start-of-exposure of multiple grabber-controlled area-scan cameras.

Accurate synchronization of the start-of-cycle, start-of-scan and end-of-scan of multiple grabber-controlled line-scan cameras.

Specification

C2C-Link synchronizes cameras connected to:

- the same card
- to different cards in the same PC (requires an accessory cable such as the "3303 C2C-Link Ribbon Cable" or a custom-made C2C-Link cable)
- to different cards in different PCs (requires one "1636 InterPC C2C-Link Adapter" for each PC and one RJ 45 CAT 5 STP straight LAN cable for each adapter but the last one)

Maximum distance:

- 120 cm inside a PC
- 1200 m cumulated adapter to adapter cable length

Maximum trigger rate:

- 2.5 MHz for configurations using a single PC, or up to 10 PCs and 100 m total C2C-Link cable length
- 200 kHz for configurations up to 32 PCs and 1200m total C2C-Link cable length

Trigger propagation delay from master to slave devices:

- Less than 10 ns for cameras on the same card or on different cards in the same PC
- Less than 265 ns for cameras on different cards in different PCs (3 PCs and 40m total C2C-Link cable length)

Software

Host PC Operating System

Microsoft Windows 11, 10 for x86-64 (64-bit) processor architecture

Linux for x86-64 (64-bit) and AArch64 (64-bit) processor architectures

macOS for x86-64 (64-bit) and AArch64 (64-bit) processor architectures

APIs

EGrabber class, with C++ and .NET APIs: .NET assembly designed to be used with development environments compatible with .NET frameworks version 4.0 or higher

GenICam GenTL producer libraries compatible with C/C++ compilers:

'x86_64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of x86-64 (64-bit) applications

'aarch64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of AArch64 (64-bit) applications

Memento supported

Yes

Environmental conditions

Operating ambient air temperature

0 °C to +55 °C / +32 °F to +131 °F

Operating ambient air humidity

10% to 90% RH non-condensing

Storage ambient air temperature

-20 °C to +70 °C / -4 °F to +158 °F

Storage ambient air humidity

10% to 90% RH non-condensing

Certifications

EMC standards

European Council EMC Directive 2014/30/EU

United States FCC rule 47 CFR 15

EMC - Emission

EN 55032:2015 / CISPR 32:2012 Class A

FCC 47 Part 15 Class A

EMC - Immunity

EN 55035:2017 / CISPR 35:2016

EN 61000-6-2:2005 / IEC 61000-6-2:2016

EN 61000-4-2:2009

EN 61000-4-3:2006

EN 61000-4-4:2004

EN 61000-4-6:2014

KC Certification

Korean Radio Waves Act, Article 58-2, Clause 3

Flammability

PCB compliant with UL 94 V-0

RoHS

European Union Directive [2015/863](#) (ROHS3)

REACH

European Union Regulation [1907/2006](#)

WEEE

Must be disposed of separately from normal household waste and must be recycled according to local regulations

Ordering Information

Product code - Description

PC3624 Coaxlink Quad CXP-12 DF

Related products

PC1625 DB25F I/O Adapter Cable

PC1636 InterPC C2C-Link Adapter

PC3303 C2C-Link Ribbon Cable

PC3304 HD26F I/O Adapter Cable

PC3610 HD26F I/O Extension Module - TTL-RS422

PC3612 HD26F I/O Extension Module - TTL-CMOS5V-RS422

PC3614 HD26F I/O Extension Module - Standard I/O Set

PC3618 HD26F I/O Extension Module - Fast I/O

Offices

- Europe, Middle East & Africa
Euresys SA
Contact support : support.europe@euresys.com

Sensor to Image GmbH
Contact support : support.europe@euresys.com
- China
Euresys Shanghai Liaison Office
Contact support : support.china@euresys.com

Euresys Shenzhen Liaison Office
Contact support : support.china@euresys.com
- Japan
Euresys Japan K.K.
Contact support : support.japan@euresys.com
- South Korea
Euresys South Korea Liaison Office
Contact support : support.korea@euresys.com
- Asia (other countries)
Euresys Pte. Ltd.
Contact support : support.asia@euresys.com
- North, Central & South America
Euresys Inc.
Contact support : support.usa@euresys.com

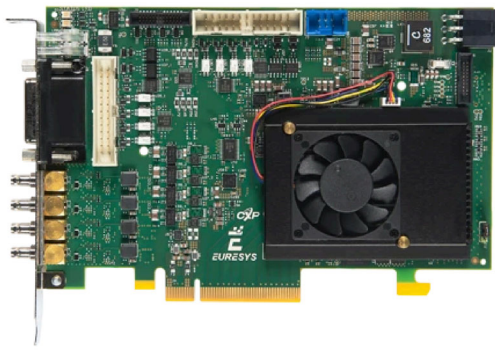
TKH Vision Experience Center
Contact support : support.usa@euresys.com

6/3/2024

Datasheet

Coaxlink Quad CXP-12

Four-connection CoaXPress CXP-12 frame grabber



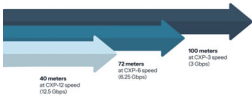
- Four CoaXPress CXP-12 connections: 5,000 MB/s camera bandwidth
- PCIe 3.0 (Gen 3) x8 bus: 6,700 MB/s bus bandwidth
- Feature-rich set of 20 digital I/O lines
- Extensive camera control functions
- Memento Event Logging Tool
- Compatible with CustomLogic: Your own FPGA logic

Main benefits



PCIe 3.0 (Gen 3) x8 bus

- 7,800 MB/s peak bus bandwidth
- 6,700 MB/s sustained bus bandwidth



Long cable support

- 40 meters at CXP-12 speed (12.5 Gbps)
- 72 meters at CXP-6 speed (6.25 Gbps)
- 100 meters at CXP-3 speed (3 Gbps)



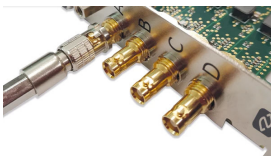
Power over CoaXPress

- Power over CoaXPress : Feed your camera up to 17 W per channel under 24 VDC with automatic device detection, measurement and overload protection.
- Total and per-channel voltage and current measurement is possible, allowing validation and performance deviation monitoring.



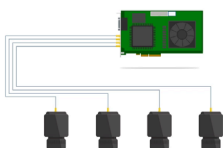
Use standard coaxial cables

- A single inexpensive cable for data transfer, camera control, trigger and power supply
- Top reliability and flexibility, performs in the harshest environments



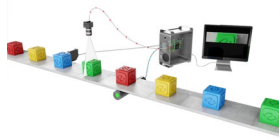
Micro-BNC (HD-BNC™) connectors for reliable connection

- Trusted push and turn, bayonet-style positive lock
- Allows for quick and easy connects and disconnects



Connect up to 4 cameras to a single Coaxlink card

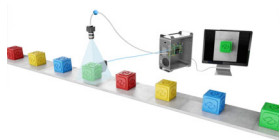
Connect up to 4 cameras to a single Coaxlink card



Line-scan triggering capabilities

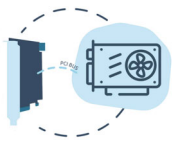
Euresys' frame grabbers offer many capabilities to synchronize line-scan or 1D cameras, sensors and lighting controllers. Frame grabbers can control the camera scanning rate based on the signals received from a motion encoder.

They support continuous web scanning (to inspect infinite, continuously moving surfaces without losing a single line) and discrete object scanning (to acquire the image of objects moving in front of the camera).



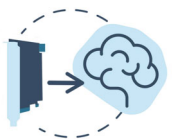
Area-scan triggering capabilities

Euresys' frame grabbers offer many capabilities to synchronize area-scan or 2D cameras, sensors and lighting controllers, for stationery or moving objects in the field of view, or moving cameras.



Direct GPU transfer

- Sample programs for AMD DirectGMA and NVIDIA (CUDA) available.
- Direct GPU transfer eliminates unnecessary system memory copies, lowers CPU overhead, and reduces latency, resulting in significant performance improvements in data transfer times for applications.
- Direct capture of image data to GPU memory is available using AMD's DirectGMA. Compatible with AMD FirePro W5x00 and above and all AMD FirePro S series products.



High-performance DMA (Direct Memory Access)

- Direct transfer into user-allocated memory
- Hardware scatter-gather support



Compatible with eGrabber

- eGrabber Studio: eGrabber's new interactive evaluation and demonstration application
- GenICam Browser: An application giving access to the GenICam features exposed by the GenTL Producer(s)
- GenTL Console: A command-line tool giving access to the functions and commands exposed by the Euresys GenTL Producer



C2C-Link camera synchronization

Allows to accurately synchronize multiple area-scan or line-scan cameras connected

- to the same card
- to different cards in the same PC
- to different cards in different PCs

ARM macOS



Windows, Linux and macOS drivers available

Including support for Intel 64-bit platforms as well as ARM 64-bit platforms.

Other benefits

Line-scan Metadata insertion

When activated, this feature records metadata beside image data. Line metadata are captured every acquired image line. Buffer metadata are only captured when the first image line of a buffer is acquired. The metadata are composed with a configurable set of general purpose event counters, quadrature encoder position counters and/or I/O line status. This feature allows line-scan applications to correlate image data with system events including motion encoder positions.

Flexible line-scan camera operation with the rate converter

- The rate converter is a smart, programmable frequency multiplier/divider.
 - Used with motion encoders and line-scan cameras, it allows the user to choose the aspect ratio of the pixels in the image.
 - It provides a way to calibrate the acquisition chain to easily reach square (1:1 aspect ratio) pixels.
-

General purpose I/O lines

- Compatible with a wide range of sensors and motion encoders.
- High-speed differential inputs: Quadrature motion encoder support up to 5 MHz.
- Isolated current-sense inputs: 5V, 12V, 24V signaling voltages accepted, up to 50 kHz, individual galvanic isolation up to 250VDC and 170VAC RMS.
- Isolated contact outputs.
- High-speed 5V-compliant TTL inputs/ LVTTTL outputs.

Specifications

Mechanical

Form factor

PCI Express card

Format

Standard profile, half length, 8-lane PCI Express card

Cooling method

Air cooling, fan-cooled heatsink

Mounting

For insertion in a standard height, 8-lane or higher, PCI Express card slot

Connectors

'A B C D' on card bracket:

4 x Micro-BNC 75 Ohms coaxial receptacles

CoaXPress Host Interface

'EXTERNAL I/O' on card bracket:

26-pin 3-row high-density D-Sub female socket with UNC4-40 jack socket screws

I/O lines and I/O power output

'INTERNAL I/O 1' on printed circuit board:

26-pin 2-row 0.1" pitch pin header with shrouding

I/O lines and I/O power output

'INTERNAL I/O 2' on printed circuit board:

26-pin 2-row 0.1" pitch pin header with shrouding

I/O lines and I/O power output

'I/O EXTENSION' on printed circuit board:

26-pin 2-row 0.05" pitch pin header with shrouding

I/O extension cable socket

'C2C-LINK' on printed circuit board:

6-pin 2-row 0.1" pitch pin header with shrouding

Card-to-card link

'AUXILIARY POWER INPUT' on printed circuit board:

6-pin PEG power socket

12 V DC power input for PoCXP and I/O power output

LED indicators

'A', 'B', 'C', 'D' on bracket:

Bi-color red/green LEDs

CoaXPress Host connector indicator

'FPGA STATUS LAMP' on PCB:

Bi-color red/green LED

FPGA status indicator

'BOARD STATUS LAMP' on PCB:

Bi-color red/green LED

Board status indicator

Switches

'RECOVERY' on PCB:

3-pin 1-row 0.1" header or 2-way DIP switch

Firmware emergency recovery

Dimensions

PCB L x H: 167.65 mm x 111.15 mm [6.6 in x 4.38 in]

Weight

Net weight: 196 g [6.9 oz]

Gross weight: 297 g [10.5 oz]

Host bus

Standard

PCI Express 3.0

Link width

8 lanes

1 lane, 2 lanes or 4 lanes with reduced performance

Link speed

8.0 GT/s (PCIe 3.0)

5.0 GT/s (PCIe 2.0) with reduced performance

Maximum payload size

512 bytes

DMA

32- and 64-bit

Peak delivery bandwidth

7,800 MB/s

Effective (sustained) delivery bandwidth

6,700 MB/s (Host PC motherboard dependent)

Power consumption

Typ. 17.1 W (7.3 W @ +3.3V, 9.8 W @ +12V), excluding camera and I/O power output

Camera / video inputs

Camera interface standard

CoaXPress

Interface standard(s)

CoaXPress 1.0, 1.1, 1.1.1, 2.0 and 2.1

Maximum link speed

CXP-12

Maximum link width

4 connections

Camera powering

PoCXP

Connectors

Four micro-BNC 75 Ohms (also known as HD-BNC™) CXP-12

Status LEDs

One CoaXPress Host connection status LED per connection

Number of cameras

Area-scan cameras:

- One 1- or 2- or 4-connection camera
- Two 1- or 2-connection cameras
- Four 1-connection cameras

Line-scan cameras:

- One 1- or 2- or 4-connection camera
- Two 1- or 2-connection cameras
- Four 1-connection cameras

Maximum number of cameras

4

Line-scan cameras supported

Yes

Maximum aggregated camera data transfer rate

50 Gbps (5,000 MB/s)

Supported CXP down-connection speeds

1.25 Gbps (CXP-1), 2.5 Gbps (CXP-2), 3.125 Gbps (CXP-3), 5 Gbps (CXP-5), 6.25 Gbps (CXP-6), 10.0 Gbps (CXP-10), and 12.5 Gbps (CXP-12)

Supported CXP up-connection speeds

Low-speed 20.83... Mbps (CXP-1 to CXP-6)

Low-speed 41.66... Mbps (CXP-10, CXP-12)

Number of CXP data streams (per camera)

1 data stream per camera

Maximum CXP stream packet size

16,384 bytes

PoCXP (Power over CoaXPress)

PoCXP Safe Power:

- 17 W of 24V DC regulated power per CoaXPress connector

- PoCXP Device detection and automatic power-on

- Overload and short-circuit protections

On-board 12V to 24V DC/DC converter

A +12V power source must be connected to the AUXILIARY POWER INPUT connector using a 6-pin PEG cable

Camera types

Area-scan cameras:

- Grayscale and color (YCbCr, YUV, RGB and Bayer CFA)

- Single-tap (1X-1Y) progressive-scan

- Two-tap (1X-2YE) on two distinct data streams ('1-camera' firmware variant only)

Line-scan cameras and contact imaging sensors:

- Grayscale and color RGB

Camera pixel formats supported

Mono8, Mono10, Mono12, Mono14, Mono16

BayerXX8, BayerXX10, BayerXX12, BayerXX14, BayerXX16 where XX = GR, RG, GB, or BG

RGB8, RGB10, RGB12, RGB14, RGB16
RGBA8, RGBA10, RGBA12, RGBA14, RGBA16
YCbCr601_422_8, YCbCr601_422_10
YCbCr709_422_8, YCbCr709_422_10
YUV422_8, YUV422_10
Raw

Area-scan camera control

Trigger

Precise control of asynchronous reset cameras, with exposure control.
Support of camera exposure/readout overlap.
Support of external hardware trigger, with optional delay and trigger decimation.

Strobe

Accurate control of the strobe position for strobed light sources.
Support of early and late strobe pulses.

Line-scan camera control

Scan/page trigger

Precise control of start-of-scan and end-of-scan triggers.
Support of external hardware trigger, with optional delay.
Support of infinite acquisition, without missing line, for web inspection applications.

Line trigger

Support for quadrature motion encoders, with programmable noise filters, selection of acquisition direction and backward motion compensation.
Rate Converter tool for fine control of the pixel aspect ratio: Rate Conversion Ratio in the range 0.001 to 1000 with an accuracy better than 0.1%.
Rate Divider tool

Line strobe

Accurate control of the strobe position for strobed light sources.

On-board processing

On-board memory

4 GB

Image data stream processing

Unpacking of 10-/12-/14-bit to 16-bit with selectable justification to LSB or MSb

Optional swap of R and B components

Little endian conversion

Flat-field correction

Only available with the '1-camera' firmware variant

Input LUT (Lookup Table)

Monochrome 8-bit to 8-bit transformation

Monochrome 10-bit to 8-, 10- or 16-bit transformations

Monochrome 12-bit to 8-, 12- or 16-bit transformations

Bayer CFA to RGB decoder

'1-camera' firmware variant:

3x3 linear interpolation method

3x3 median-based interpolation method

Pixel binning

'1-camera' firmware variant:

2x2 and 4x4 binning windows

Bypass, sum and mean methods

CustomLogic

Yes

CustomLogic firmware variants

'1-camera, customlogic' firmware variant for one 1- or 2- or 4-connection area-scan camera

'1-camera, line-scan, customlogic' firmware variant for one 1- or 2- or 4-connection line-scan camera

'2-camera, customlogic' firmware variant for two 1- or 2-connection area-scan cameras

'4-camera, customlogic' firmware variant for four 1-connection area-scan cameras

Data stream statistics

Measurement of:

Frame rate (Area-scan only)

Line rate

Data rate

Configurable averaging interval

Event signaling and counting

The application software can be notified of the occurrence of various events:

Standard event: the EVENT_NEW_BUFFER event notifies the application of newly filled buffers

A large set of custom events

Custom events sources:

- I/O Toolbox events
- Camera and Illumination control events
- CoaXPress data stream events
- CoaXPress host interface events

Each custom event is associated with a 32-bit counter that counts the number of occurrences

The last three 32-bit context data words of the event context data can be configured with event-specific context data:

- Event-specific data
- State of all System I/O lines sampled at the event occurrence time
- Value of any event counter

General Purpose Inputs and Outputs

Number of lines

20 I/O lines:

- 4 differential inputs (DIN)
- 4 singled-ended TTL inputs/outputs (TTLIO)
- 8 isolated inputs (IIN)
- 4 isolated outputs (IOUT)

NOTE: The number of I/O lines can be extended using I/O modules attached to the I/O EXTENSION connector.

Usage

Any I/O input lines can be used by any LIN tool of the I/O Toolbox

Selected pairs of I/O input lines can be used by any QDC tool of the I/O toolbox to decode A/B signals of a motion encoder

Electrical specifications

DIN: High-speed differential inputs, up to 5 MHz, compatible with ANSI/EIA/TIA-422/485 differential line drivers and complementary TTL drivers

TTLIO: High-speed 5V-compliant TTL inputs or LVTTTL outputs, compatible with totem-pole LVTTTL, TTL, 5V CMOS drivers or LVTTTL, TTL, 3V CMOS receivers

IIN: Isolated current-sense inputs with wide voltage input range up to 30V, compatible with totem-pole LVTTTL, TTL, 5V CMOS drivers, RS-422 differential line drivers, potential free contacts, solid-state relays and opto-couplers

IOUT: Isolated contact outputs compatible with 30V / 100mA loads

NOTE: IIN and IOUT lines provide a functional isolation grade for the circuit technical protection. It does not provide an isolation that can protect a human being from electrical shock!

Filter control

Glitch removal filter available on all System I/O input lines

Configurable filter delay:

- Custom value
- Fixed values for DIN and TTLIO lines: 50 ns, 100 ns, 200 ns, 500 ns, 1 μ s

Fixed values for IIN lines: 500 ns, 1 μ s, 2 μ s, 5 μ s, 10 μ s

Polarity control

Yes

Power output

Non-isolated, +12V, 1A, with electronic fuse protection

I/O Toolbox tools

The I/O Toolbox is a configurable interconnection of tools that generates events (usually triggers):

Line Input tool (LIN): edge detector delivering events on rising or falling edges of any selected input line.

Quadrature Decoder tool (QDC): a composite tool including:

- A quadrature edge detector delivering events on selected transitions of selected pairs of input lines.
- An optional backward motion compensator for clean line-scan image acquisition when the motion is unstable.
- A 32-bit up/down counter for delivering a position value.

Device Link Trigger tool (DLT): delivers an event on reception of a valid high-speed CoaXPress 2.0 connection trigger packet message from the remote device.

User Actions Scheduler tool (UAS): to delegate the execution of 'User Actions' at a scheduled time or encoder position. Possible user actions include setting low/high/toggle any bit of the User Output Register or generation of any User Events.

Delay tool (DEL): to delay up to 16 events from one or two I/O toolbox event sources, by a programmable time or number of motion encoder ticks (any QDC events).

Divider tool (DIV): to generate an event every nth input events from any I/O toolbox event source.

Multiplier/divider tool (MDV): to generate m events every d input events from any I/O toolbox event source.

The 'Input Tools' (LIN, QDC, DLT and UAS) can be further processed by the 'Event Tools' (DEL, DIV and MDV) to generate any of the following "trigger" events:

- The "cycle trigger" of the Camera and Illumination controller
- The "cycle sequence trigger" of the Camera and Illumination controller
- The "start-of-scan trigger" of the Acquisition Controller (line-scan only)
- The "end-of-scan trigger" of the Acquisition Controller (line-scan only)

I/O Toolbox composition

Determined by the selected firmware variant:

'1-camera': 8 LIN, 1 QDC, 2 DLT, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 2 C2C

'2-camera': 8 LIN, 1 QDC, 4 DLT, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 2 C2C

'4-camera': 8 LIN, 4 QDC, 8 DLT, 1 UAS, 2 DEL, 4 DIV, 4 MDV, 2 C2C

'1-camera, line-scan': 8 LIN, 1 QDC, 2 DLT, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 3 C2C

'2-camera, line-scan': 8 LIN, 2 QDC, 4 DLT, 1 UAS, 2 DEL, 2 DIV, 2 MDV, 3 C2C

'4-camera, line-scan': 8 LIN, 4 QDC, 8 DLT, 1 UAS, 4 DEL, 4 DIV, 4 MDV, 3 C2C

C2C-Link

Description

Accurate synchronization of the trigger and the start-of-exposure of multiple grabber-controlled area-scan cameras.

Accurate synchronization of the start-of-cycle, start-of-scan and end-of-scan of multiple grabber-controlled line-scan cameras.

Specification

C2C-Link synchronizes cameras connected to:

- the same card

- to different cards in the same PC (requires an accessory cable such as the "3303 C2C-Link Ribbon Cable" or a custom-made C2C-Link cable)

- to different cards in different PCs (requires one "1636 InterPC C2C-Link Adapter" for each PC and one RJ 45 CAT 5 STP straight LAN cable for each adapter but the last one)

Maximum distance:

- 120 cm inside a PC

- 1200 m cumulated adapter to adapter cable length

Maximum trigger rate:

- 2.5 MHz for configurations using a single PC, or up to 10 PCs and 100 m total C2C-Link cable length

- 200 kHz for configurations up to 32 PCs and 1200m total C2C-Link cable length

Trigger propagation delay from master to slave devices:

- Less than 10 ns for cameras on the same card or on different cards in the same PC

- Less than 265 ns for cameras on different cards in different PCs (3 PCs and 40m total C2C-Link cable length)

Software

Host PC Operating System

Microsoft Windows 11, 10 for x86-64 (64-bit) processor architecture

Linux for x86-64 (64-bit) and AArch64 (64-bit) processor architectures

macOS for x86-64 (64-bit) and AArch64 (64-bit) processor architectures

APIs

EGrabber class, with C++ and .NET APIs: .NET assembly designed to be used with development environments compatible with .NET frameworks version 4.0 or higher

GenICam GenTL producer libraries compatible with C/C++ compilers:

- 'x86_64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of x86-64 (64-bit) applications

- 'aarch64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of AArch64 (64-bit) applications

Memento supported

Yes

Environmental conditions

Operating ambient air temperature

0 °C to +55 °C / +32 °F to +131 °F

Operating ambient air humidity

10% to 90% RH non-condensing

Storage ambient air temperature

-20 °C to +70 °C/ -4 °F to +158 °F

Storage ambient air humidity

10% to 90% RH non-condensing

Certifications

EMC standards

European Council EMC Directive 2014/30/EU

United States FCC rule 47 CFR 15

EMC - Emission

EN 55032:2015 / CISPR 32:2012 Class B

FCC 47 Part 15 Class B

EMC - Immunity

EN 55024:2010 / CISPR 24:2010

EN 55035:2017 / CISPR 35:2016

EN 61000-4-2:2009

EN 61000-4-3:2006

EN 61000-4-4:2004

EN 61000-4-6:2014

KC Certification

Korean Radio Waves Act, Article 58-2, Clause 3

Flammability

PCB compliant with UL 94 V-0

RoHS

European Union Directive [2015/863](#) (ROHS3)

REACH

European Union Regulation [1907/2006](#)

WEEE

Must be disposed of separately from normal household waste and must be recycled according to local regulations

Ordering Information

Product code - Description

PC3603-4 Coaxlink Quad CXP-12

Related products

PC1625 DB25F I/O Adapter Cable

PC1636 InterPC C2C-Link Adapter

PC3303 C2C-Link Ribbon Cable

PC3304 HD26F I/O Adapter Cable

PC3610 HD26F I/O Extension Module - TTL-RS422

PC3612 HD26F I/O Extension Module - TTL-CMOS5V-RS422

PC3613 JTAG Adapter Xilinx for Coaxlink

Offices

- Europe, Middle East & Africa
Euresys SA
Contact support : support.europe@euresys.com

Sensor to Image GmbH
Contact support : support.europe@euresys.com
- China
Euresys Shanghai Liaison Office
Contact support : support.china@euresys.com

Euresys Shenzhen Liaison Office
Contact support : support.china@euresys.com
- Japan
Euresys Japan K.K.
Contact support : support.japan@euresys.com
- South Korea
Euresys South Korea Liaison Office
Contact support : support.korea@euresys.com
- Asia (other countries)
Euresys Pte. Ltd.
Contact support : support.asia@euresys.com
- North, Central & South America
Euresys Inc.
Contact support : support.usa@euresys.com

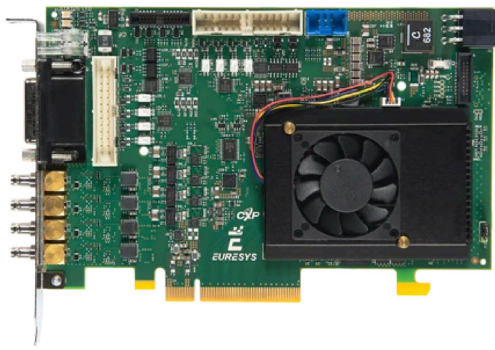
TKH Vision Experience Center
Contact support : support.usa@euresys.com

5/22/2024

Datasheet

Coaxlink Quad CXP-12 JPEG

Four-connection CoaXPress CXP-12 frame grabber with JPEG compression



- Four 250 MPixels/s JPEG encoders
- Compatible with 8-bit/pixel Bayer CFA cameras
- Two streams per camera: JPEG stream and RGB preview stream
- Four CoaXPress CXP-12 connections: 5,000 MB/s camera bandwidth
- PCIe 3.0 (Gen 3) x8 bus: 6,700 MB/s bus bandwidth
- Memento Event Logging Tool

Main benefits



Acquire images from the fastest and highest resolution cameras

- Highest data acquisition rate in the industry
 - Up to 5,000 MB/s bandwidth from camera to host PC memory
-



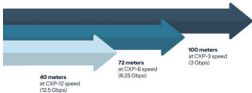
PCIe 3.0 (Gen 3) x8 bus

- 7,800 MB/s peak bus bandwidth
 - 6,700 MB/s sustained bus bandwidth
-



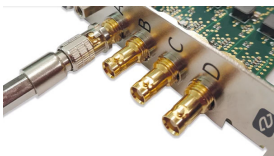
Power over CoaXPress

- Power over CoaXPress : Feed your camera up to 17 W per channel under 24 VDC with automatic device detection, measurement and overload protection.
 - Total and per-channel voltage and current measurement is possible, allowing validation and performance deviation monitoring.
-



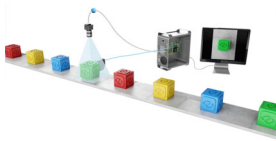
Long cable support

- 40 meters at CXP-12 speed (12.5 Gbps)
 - 72 meters at CXP-6 speed (6.25 Gbps)
 - 100 meters at CXP-3 speed (3 Gbps)
-



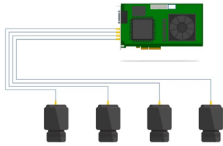
Micro-BNC (HD-BNC™) connectors for reliable connection

- Trusted push and turn, bayonet-style positive lock
- Allows for quick and easy connects and disconnects



Area-scan triggering capabilities

Euresys' frame grabbers offer many capabilities to synchronize area-scan or 2D cameras, sensors and lighting controllers, for stationary or moving objects in the field of view, or moving cameras.



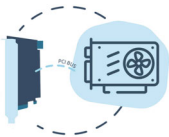
Connect up to 4 cameras to a single Coaxlink card

Connect up to 4 cameras to a single Coaxlink card



Use standard coaxial cables

- A single inexpensive cable for data transfer, camera control, trigger and power supply
- Top reliability and flexibility, performs in the harshest environments



Direct GPU transfer

- Sample programs for AMD DirectGMA and NVIDIA (CUDA) available.
- Direct GPU transfer eliminates unnecessary system memory copies, lowers CPU overhead, and reduces latency, resulting in significant performance improvements in data transfer times for applications.
- Direct capture of image data to GPU memory is available using AMD's DirectGMA. Compatible with AMD FirePro W5x00 and above and all AMD FirePro S series products.



Compatible with eGrabber

- [eGrabber Studio](#): eGrabber's new interactive evaluation and demonstration application
- [GenICam](#) Browser: An application giving access to the GenICam features exposed by the GenTL Producer(s)
- GenTL Console: A command-line tool giving access to the functions and commands exposed by the Euresys GenTL Producer



Memento Event Logging Tool

- **Memento** is an advanced development and debugging tool available for Coaxlink and Grablink cards.
- Memento records an accurate log of all the events related to the camera, the frame grabber and its driver as well as the application.
- It provides the developer with a precise timeline of time-stamped events, along with context information and logic analyzer view.
- It provides valuable assistance during application development and debugging, as well as during machine operation.



High-performance DMA (Direct Memory Access)

- Direct transfer into user-allocated memory
- Hardware scatter-gather support



Compliant with GenICam

Including support for:

- GenApi
- The Standard Feature Naming Convention (SFNC)
- GenTL



Windows, Linux and macOS drivers available

Including support for Intel 64-bit platforms as well as ARM 64-bit platforms.



Other benefits

Applications

The Coaxlink Quad CXP-12 JPEG enables the compact implementation of a multi-channel ultra-high-resolution image acquisition and recording system. The embedded pixel processing drastically reduces the CPU workload to monitor and compress image streams.

Support of JFIF image format

The GenICam Browser and GenTL Viewer applications now support JFIF images.

Specifications

Mechanical

Form factor

PCI Express card

Format

Standard profile, half length, 8-lane PCI Express card

Cooling method

Air cooling, fan-cooled heatsink

Mounting

For insertion in a standard height, 8-lane or higher, PCI Express card slot

Connectors

'A B C D' on card bracket:

4 x Micro-BNC 75 Ohms coaxial receptacles

CoaXPress Host Interface

'EXTERNAL I/O' on card bracket:

26-pin 3-row high-density D-Sub female socket with UNC4-40 jack socket screws

I/O lines and I/O power output

'INTERNAL I/O 1' on printed circuit board:

26-pin 2-row 0.1" pitch pin header with shrouding

I/O lines and I/O power output

'INTERNAL I/O 2' on printed circuit board:

26-pin 2-row 0.1" pitch pin header with shrouding

I/O lines and I/O power output

'I/O EXTENSION' on printed circuit board:

26-pin 2-row 0.05" pitch pin header with shrouding

I/O extension cable socket

'C2C-LINK' on printed circuit board:

6-pin 2-row 0.1" pitch pin header with shrouding

Card-to-card link

'AUXILIARY POWER INPUT' on printed circuit board:

6-pin PEG power socket

12 V DC power input for PoCXP and I/O power output

LED indicators

'A', 'B', 'C', 'D' on bracket:

Bi-color red/green LEDs

CoaXPress Host connector indicator

'FPGA STATUS LAMP' on PCB:

Bi-color red/green LED

FPGA status indicator

'BOARD STATUS LAMP' on PCB:

Bi-color red/green LED

Board status indicator

Switches

'RECOVERY' on PCB:

3-pin 1-row 0.1" header or 2-way DIP switch

Firmware emergency recovery

Dimensions

PCB L x H: 167.65 mm x 111.15 mm [6.6 in x 4.38 in]

Weight

Net weight: 196 g [6.9 oz]

Gross weight: 297 g [10.5 oz]

Host bus

Standard

PCI Express 3.0

Link width

8 lanes

1 lane, 2 lanes or 4 lanes with reduced performance

Link speed

8.0 GT/s (PCIe 3.0)

5.0 GT/s (PCIe 2.0) with reduced performance

Maximum payload size

512 bytes

DMA

32- and 64-bit

Peak delivery bandwidth

7,800 MB/s

Effective (sustained) delivery bandwidth

6,700 MB/s (Host PC motherboard dependent)

Power consumption

Typ. 18.1 W (6.3 W @ +3.3V, 11.8 W @ +12V), excluding camera and I/O power output

Camera / video inputs

Camera interface standard

CoaXPress

Interface standard(s)

CoaXPress 1.0, 1.1, 1.1.1, 2.0 and 2.1

Maximum link speed

CXP-12

Maximum link width

4 connections

Camera powering

PoCXP

Connectors

Four micro-BNC 75 Ohms (also known as HD-BNC™) CXP-12

Status LEDs

One CoaXPress Host connection status LED per connection

Number of cameras

Four 1-connection area-scan cameras

Maximum number of cameras

4

Maximum aggregated camera data transfer rate

50 Gbps (5,000 MB/s)

Supported CXP down-connection speeds

1.25 Gbps (CXP-1), 2.5 Gbps (CXP-2), 3.125 Gbps (CXP-3), 5 Gbps (CXP-5), 6.25 Gbps (CXP-6), 10.0 Gbps (CXP-10), and 12.5 Gbps (CXP-12)

Supported CXP up-connection speeds

Low-speed 20.83... Mbps (CXP-1 to CXP-6)

Low-speed 41.66... Mbps (CXP-10, CXP-12)

Number of CXP data streams (per camera)

1 data stream per camera

Maximum CXP stream packet size

16,384 bytes

PoCXP (Power over CoaXPress)

PoCXP Safe Power:

- 17 W of 24V DC regulated power per CoaXPress connector

- PoCXP Device detection and automatic power-on

- Overload and short-circuit protections

On-board 12V to 24V DC/DC converter

A +12V power source must be connected to the AUXILIARY POWER INPUT connector using a 6-pin PEG cable

Camera types

Area-scan cameras:

8-bit Bayer CFA single-tap (1X-1Y) progressive-scan

Image resolution (H x V): from 128 x 16 up to 5120 x 3840; width and height must be multiples of 8

Camera pixel formats supported

BayerGR8, BayerRG8, BayerGB8, BayerBG8

Area-scan camera control

Trigger

Precise control of asynchronous reset cameras, with exposure control.

Support of camera exposure/readout overlap.

Support of external hardware trigger, with optional delay and trigger decimation.

Strobe

Accurate control of the strobe position for strobed light sources.

Support of early and late strobe pulses.

On-board processing

On-board memory

4 GB

Image data stream processing

Optional swap of R and B components

1:8 image downscaling available on RGB8 output (Stream0, a.k.a. "preview stream")

Bayer CFA to RGB decoder

3x3 median-based interpolation method on '4-camera' firmware variant

-

Data stream statistics

Measurement of:

- Frame rate (Area-scan only)

- Line rate

- Data rate

Configurable averaging interval

Event signaling and counting

The application software can be notified of the occurrence of various events:

- Standard event: the EVENT_NEW_BUFFER event notifies the application of newly filled buffers

- A large set of custom events

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- I/O Toolbox events

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- CoaXPress data stream events

- CoaXPress host interface events

Each custom event is associated with a 32-bit counter that counts the number of occurrences

The last three 32-bit context data words of the event context data can be configured with event-specific context data:

- Event-specific data

- State of all System I/O lines sampled at the event occurrence time

- Value of any event counter

On-board video codec

On-board video compression

Yes

Video encoders

JPEG

Baseline profile

4 encoders

Up to 250 Mpixels/second per encoder

JFIF compliant output

General Purpose Inputs and Outputs

Number of lines

20 I/O lines:

4 differential inputs (DIN)

4 singled-ended TTL inputs/outputs (TTLIO)

8 isolated inputs (IIN)

4 isolated outputs (IOOUT)

NOTE: The number of I/O lines can be extended using I/O modules attached to the I/O EXTENSION connector.

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IOOUT: Isolated contact outputs compatible with 30V / 100mA loads

NOTE: IIN and IOOUT lines provide a functional isolation grade for the circuit technical protection. It does not provide an isolation that can protect a human being from electrical shock!

Filter control

Glitch removal filter available on all System I/O input lines

Configurable filter delay:

Custom value

Fixed values for DIN and TTLIO lines: 50 ns, 100 ns, 200 ns, 500 ns, 1 μ s

Fixed values for IIN lines: 500 ns, 1 μ s, 2 μ s, 5 μ s, 10 μ s

Polarity control

Yes

Power output

Non-isolated, +12V, 1A, with electronic fuse protection

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Quadrature Decoder tool (QDC): a composite tool including:

- A quadrature edge detector delivering events on selected transitions of selected pairs of input lines.
- An optional backward motion compensator for clean line-scan image acquisition when the motion is unstable.
- A 32-bit up/down counter for delivering a position value.

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- The "cycle trigger" of the Camera and Illumination controller
- The "cycle sequence trigger" of the Camera and Illumination controller
- The "start-of-scan trigger" of the Acquisition Controller (line-scan only)
- The "end-of-scan trigger" of the Acquisition Controller (line-scan only)

I/O Toolbox composition

8 LIN, 4 QDC, 8 DLT, 1 UAS, 4 DEL, 4 DIV, 4 MDV, 2 C2C

C2C-Link

Description

Accurate synchronization of the trigger and the start-of-exposure of multiple grabber-controlled area-scan cameras.

Accurate synchronization of the start-of-cycle, start-of-scan and end-of-scan of multiple grabber-controlled line-scan cameras.

Specification

C2C-Link synchronizes cameras connected to:

- the same card

to different cards in the same PC (requires an accessory cable such as the "3303 C2C-Link Ribbon Cable" or a custom-made C2C-Link cable)

to different cards in different PCs (requires one "1636 InterPC C2C-Link Adapter" for each PC and one RJ 45 CAT 5 STP straight LAN cable for each adapter but the last one)

Maximum distance:

120 cm inside a PC

1200 m cumulated adapter to adapter cable length

Maximum trigger rate:

2.5 MHz for configurations using a single PC, or up to 10 PCs and 100 m total C2C-Link cable length

200 kHz for configurations up to 32 PCs and 1200m total C2C-Link cable length

Trigger propagation delay from master to slave devices:

Less than 10 ns for cameras on the same card or on different cards in the same PC

Less than 265 ns for cameras on different cards in different PCs (3 PCs and 40m total C2C-Link cable length)

Software

Host PC Operating System

Microsoft Windows 11, 10 for x86-64 (64-bit) processor architecture

Linux for x86-64 (64-bit) and AArch64 (64-bit) processor architectures

macOS for x86-64 (64-bit) and AArch64 (64-bit) processor architectures

APIs

EGrabber class, with C++ and .NET APIs: .NET assembly designed to be used with development environments compatible with .NET frameworks version 4.0 or higher

GenICam GenTL producer libraries compatible with C/C++ compilers:

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'aarch64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of AArch64 (64-bit) applications

Memento supported

Yes

Environmental conditions

Operating ambient air temperature

0 °C to +55 °C / +32 °F to +131 °F

Operating ambient air humidity

10% to 90% RH non-condensing

Storage ambient air temperature

-20 °C to +70 °C/ -4 °F to +158 °F

Storage ambient air humidity

10% to 90% RH non-condensing

Certifications

EMC standards

European Council EMC Directive 2014/30/EU

United States FCC rule 47 CFR 15

EMC - Emission

EN 55032:2015 / CISPR 32:2012 Class B

FCC 47 Part 15 Class B

EMC - Immunity

EN 55024:2010 / CISPR 24:2010

EN 55035:2017 / CISPR 35:2016

EN 61000-4-2:2009

EN 61000-4-3:2006

EN 61000-4-4:2004

EN 61000-4-6:2014

KC Certification

Korean Radio Waves Act, Article 58-2, Clause 3

Flammability

PCB compliant with UL 94 V-0

RoHS

European Union Directive [2015/863](#) (ROHS3)

REACH

European Union Regulation [1907/2006](#)

WEEE

Must be disposed of separately from normal household waste and must be recycled according to local regulations

Ordering Information

Product code - Description

PC3620-4 Coaxlink Quad CXP-12 JPEG

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PC3303 C2C-Link Ribbon Cable

PC3304 HD26F I/O Adapter Cable

PC3610 HD26F I/O Extension Module - TTL-RS422

PC3612 HD26F I/O Extension Module - TTL-CMOS5V-RS422

Offices

- Europe, Middle East & Africa
Euresys SA
Contact support : support.europe@euresys.com

Sensor to Image GmbH
Contact support : support.europe@euresys.com
- China
Euresys Shanghai Liaison Office
Contact support : support.china@euresys.com

Euresys Shenzhen Liaison Office
Contact support : support.china@euresys.com
- Japan
Euresys Japan K.K.
Contact support : support.japan@euresys.com
- South Korea
Euresys South Korea Liaison Office
Contact support : support.korea@euresys.com
- Asia (other countries)
Euresys Pte. Ltd.
Contact support : support.asia@euresys.com
- North, Central & South America
Euresys Inc.
Contact support : support.usa@euresys.com

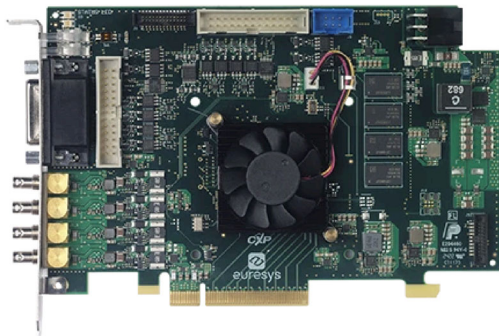
TKH Vision Experience Center
Contact support : support.usa@euresys.com

1/10/2025

Datasheet

Coaxlink Quad CXP-12 3D-LLE

Four-connection CoaXPress CXP-12 frame grabber with on-board laser line extraction for 3D profiling



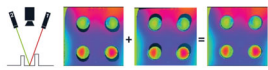
- Laser line extraction with zero host CPU usage
- Single and Dual Laser Line Extraction into a depth map
- Real-time generation of 16-bit 3D height maps
- Choice of algorithms: Maximum, Peak, Center of Gravity (COG)
- Precision: up to 1/256 pixel (with Peak and COG algorithms)
- Performance: 76,000 profiles/s from 1024 x 128 images.
152,000 profiles/s from 1024 x 64 images
- Four CoaXPress CXP-12 connections: 5,000 MB/s camera bandwidth
- Memento Event Logging Tool

Main benefits



Laser line extraction with zero host CPU usage

The Coaxlink's on-board FPGA measures the position of the laser line during the image acquisition without loading the host CPU.



Dual laser line extraction

Supported by software and hardware implementations, the dual laser line extraction process reduces the effect of occlusions. Occlusions occur when some parts of the objects are not lit by any laser. Using two lasers with different angles reduces these undefined areas. The object-based calibration included in Easy3DLaserLine allows combining the acquired data into a single calibrated point cloud.



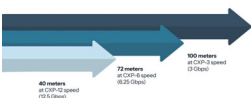
Acquire images from the fastest and highest resolution cameras

- Highest data acquisition rate in the industry
- Up to 5,000 MB/s bandwidth from camera to host PC memory



PCIe 3.0 (Gen 3) x8 bus

- 7,800 MB/s peak bus bandwidth
- 6,700 MB/s sustained bus bandwidth



Long cable support

- 40 meters at CXP-12 speed (12.5 Gbps)
- 72 meters at CXP-6 speed (6.25 Gbps)
- 100 meters at CXP-3 speed (3 Gbps)



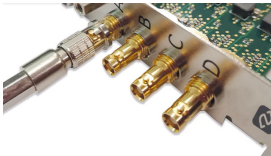
Power over CoaXPress

- Power over CoaXPress : Feed your camera up to 17 W per channel under 24 VDC with automatic device detection, measurement and overload protection.
- Total and per-channel voltage and current measurement is possible, allowing validation and performance deviation monitoring.



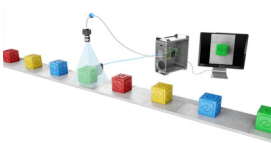
Use standard coaxial cables

- A single inexpensive cable for data transfer, camera control, trigger and power supply
- Top reliability and flexibility, performs in the harshest environments



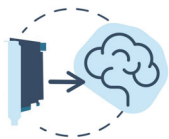
Micro-BNC (HD-BNC™) connectors for reliable connection

- Trusted push and turn, bayonet-style positive lock
- Allows for quick and easy connects and disconnects



Area-scan triggering capabilities

Euresys' frame grabbers offer many capabilities to synchronize area-scan or 2D cameras, sensors and lighting controllers, for stationery or moving objects in the field of view, or moving cameras.



High-performance DMA (Direct Memory Access)

- Direct transfer into user-allocated memory
- Hardware scatter-gather support



Compatible with eGrabber

- [eGrabber Studio](#): eGrabber's new interactive evaluation and demonstration application
- [GenICam Browser](#): An application giving access to the GenICam features exposed by the GenTL Producer(s)
- GenTL Console: A command-line tool giving access to the functions and commands exposed by the Euresys GenTL Producer



Memento Event Logging Tool

- Memento is an advanced development and debugging tool available for Coaxlink and Grablink cards.
- Memento records an accurate log of all the events related to the camera, the frame grabber and its driver as well as the application.
- It provides the developer with a precise timeline of time-stamped events, along with context information and logic analyzer view.
- It provides valuable assistance during application development and debugging, as well as during machine operation.



C2C-Link camera synchronization

Allows to accurately synchronize multiple area-scan or line-scan cameras connected

- to the same card
- to different cards in the same PC
- to different cards in different PCs



Compliant with GenICam

Including support for:

- GenApi
- The Standard Feature Naming Convention (SFNC)
- GenTL



Windows, Linux and macOS drivers available



Including support for Intel 64-bit platforms as well as ARM 64-bit platforms.

Other benefits

General purpose I/O lines

- Compatible with a wide range of sensors and motion encoders.
- High-speed differential inputs: Quadrature motion encoder support up to 5 MHz.
- Isolated current-sense inputs: 5V, 12V, 24V signaling voltages accepted, up to 50 kHz, individual galvanic isolation up to 250VDC and 170VAC RMS.
- Isolated contact outputs.
- High-speed 5V-compliant TTL inputs/ LVTTTL outputs.

Specifications

Mechanical

Form factor

PCI Express card

Format

Standard profile, half length, 8-lane PCI Express card

Cooling method

Air cooling, fan-cooled heatsink

Mounting

For insertion in a standard height, 8-lane or higher, PCI Express card slot

Connectors

'A B C D' on card bracket:

4 x Micro-BNC 75 Ohms coaxial receptacles

CoaXPress Host Interface

'EXTERNAL I/O' on card bracket:

26-pin 3-row high-density D-Sub female socket with UNC4-40 jack socket screws

I/O lines and I/O power output

'INTERNAL I/O 1' on printed circuit board:

26-pin 2-row 0.1" pitch pin header with shrouding

I/O lines and I/O power output

'INTERNAL I/O 2' on printed circuit board:

26-pin 2-row 0.1" pitch pin header with shrouding

I/O lines and I/O power output

'I/O EXTENSION' on printed circuit board:

26-pin 2-row 0.05" pitch pin header with shrouding

I/O extension cable socket

'C2C-LINK' on printed circuit board:

6-pin 2-row 0.1" pitch pin header with shrouding

Card-to-card link

'AUXILIARY POWER INPUT' on printed circuit board:

6-pin PEG power socket

12 V DC power input for PoCXP and I/O power output

LED indicators

'A', 'B', 'C', 'D' on bracket:

Bi-color red/green LEDs

CoaXPress Host connector indicator

'FPGA STATUS LAMP' on PCB:

Bi-color red/green LED

FPGA status indicator

'BOARD STATUS LAMP' on PCB:

Bi-color red/green LED

Board status indicator

Switches

'RECOVERY' on PCB:

3-pin 1-row 0.1" header or 2-way DIP switch

Firmware emergency recovery

Dimensions

PCB L x H: 167.65 mm x 111.15 mm [6.6 in x 4.38 in]

Weight

Net weight: 187 g [6.6 oz]

Gross weight: 287 g [10.1 oz]

Host bus

Standard

PCI Express 3.0

Link width

8 lanes

1 lane, 2 lanes or 4 lanes with reduced performance

Link speed

8.0 GT/s (PCIe 3.0)

5.0 GT/s (PCIe 2.0) with reduced performance

Maximum payload size

512 bytes

DMA

32- and 64-bit

Peak delivery bandwidth

7,800 MB/s

Effective (sustained) delivery bandwidth

6,700 MB/s (Host PC motherboard dependent)

Power consumption

Typ. 16.7 W (3.3 W @ +3.3V, 13.4 W @ +12V), excluding camera and I/O power output

Camera / video inputs

Camera interface standard

CoaXPress

Interface standard(s)

CoaXPress 1.0, 1.1, 1.1.1, 2.0 and 2.1

Maximum link speed

CXP-12

Maximum link width

4 connections

Camera powering

PoCXP

Connectors

Four micro-BNC 75 Ohms (also known as HD-BNC™) CXP-12

Status LEDs

One CoaXPress Host connection status LED per connection

Number of cameras

One 1- or 2- or 4-connection camera

Maximum number of cameras

1

Maximum aggregated camera data transfer rate

50 Gbps (5,000 MB/s)

Supported CXP down-connection speeds

1.25 Gbps (CXP-1), 2.5 Gbps (CXP-2), 3.125 Gbps (CXP-3), 5 Gbps (CXP-5), 6.25 Gbps (CXP-6), 10.0 Gbps (CXP-10), and 12.5 Gbps (CXP-12)

Supported CXP up-connection speeds

Low-speed 20.83... Mbps (CXP-1 to CXP-6)

Low-speed 41.66... Mbps (CXP-10, CXP-12)

Number of CXP data streams (per camera)

1 data stream per camera

Maximum CXP stream packet size

16,384 bytes

PoCXP (Power over CoaXPress)

PoCXP Safe Power:

- 17 W of 24V DC regulated power per CoaXPress connector

- PoCXP Device detection and automatic power-on

- Overload and short-circuit protections

On-board 12V to 24V DC/DC converter

A +12V power source must be connected to the AUXILIARY POWER INPUT connector using a 6-pin PEG cable

Camera types

Grayscale area-scan cameras

Camera pixel formats supported

Mono8

Area-scan camera control

Trigger

Precise control of asynchronous reset cameras, with exposure control.

Support of camera exposure/readout overlap.

Support of external hardware trigger, with optional delay and trigger decimation.

Strobe

Accurate control of the strobe position for strobed light sources.

Support of early and late strobe pulses.

On-board processing

On-board memory

4 GB

Image data stream processing

Laser Line Extraction (LLE) processing core to compute the vertical position of one detected laser line along a ROI using one of the following algorithms:

Maximum Detection algorithm

Maximum ROI width: 8192 pixels
Maximum ROI height: 65536 pixels
Depth map format: 16-bit unsigned integer number
Accuracy: 1 pixel

8-bit Maximum Detection algorithm

Maximum ROI width: 8192 pixels
Maximum ROI height: 256 pixels
Depth map format: 8-bit unsigned integer number
Accuracy: 1 pixel

Peak Detection algorithm

Maximum ROI width: 8192 pixels
Maximum ROI height: 2048 pixels
Depth map format: UQ11.5 fixed-point unsigned number
Accuracy: 1/32 pixel

High accuracy Peak Detection algorithm

Maximum ROI width: 8192 pixels
Maximum ROI height: 256 pixels
Depth map format: UQ8.8 fixed-point unsigned number
Accuracy: 1/256 pixel

Center Of Gravity algorithm

Maximum ROI width: 8192 pixels
Maximum ROI height: 2048 pixels
Depth map format: UQ11.5 fixed-point unsigned number
Accuracy: 1/32 pixel

High accuracy Center Of Gravity algorithm

Maximum ROI width: 8192 pixels
Maximum ROI height: 256 pixels
Depth map format: UQ8.8 fixed-point unsigned number
Accuracy: 1/256 pixel

Data stream statistics

Measurement of:

Frame rate (Area-scan only)
Line rate

Data rate

Configurable averaging interval

Event signaling and counting

The application software can be notified of the occurrence of various events:

Standard event: the EVENT_NEW_BUFFER event notifies the application of newly filled buffers

A large set of custom events

Custom events sources:

I/O Toolbox events

Camera and Illumination control events

CoaXPress data stream events

CoaXPress host interface events

Each custom event is associated with a 32-bit counter that counts the number of occurrences

The last three 32-bit context data words of the event context data can be configured with event-specific context data:

Event-specific data

State of all System I/O lines sampled at the event occurrence time

Value of any event counter

General Purpose Inputs and Outputs

Number of lines

20 I/O lines:

4 differential inputs (DIN)

4 singled-ended TTL inputs/outputs (TTLIO)

8 isolated inputs (IIN)

4 isolated outputs (IOUT)

NOTE: The number of I/O lines can be extended using I/O modules attached to the I/O EXTENSION connector.

Usage

Any I/O input lines can be used by any LIN tool of the I/O Toolbox

Selected pairs of I/O input lines can be used by any QDC tool of the I/O toolbox to decode A/B signals of a motion encoder

Electrical specifications

DIN: High-speed differential inputs, up to 5 MHz, compatible with ANSI/EIA/TIA-422/485 differential line drivers and complementary TTL drivers

TTLIO: High-speed 5V-compliant TTL inputs or LVTTTL outputs, compatible with totem-pole LVTTTL, TTL, 5V CMOS drivers or LVTTTL, TTL, 3V CMOS receivers

IIN: Isolated current-sense inputs with wide voltage input range up to 30V, signaling up to 200 kHz, individual galvanic isolation up to 250VDC and 170 VAC, compatible with totem-pole LVTTTL, TTL, 5V CMOS drivers, RS-422 differential line drivers, potential free contacts, solid-state relays and opto-couplers

IOUT: Isolated contact outputs compatible with 30V / 100mA loads

NOTE: IIN and IOUT lines provide a functional isolation grade for the circuit technical protection. It does not provide an isolation that can protect a human being from electrical shock!

Filter control

Glitch removal filter available on all System I/O input lines

Configurable filter delay:

Custom value

Fixed values for DIN and TTLIO lines: 50 ns, 100 ns, 200 ns, 500 ns, 1 μ s

Fixed values for IIN lines: 500 ns, 1 μ s, 2 μ s, 5 μ s, 10 μ s

Polarity control

Yes

Power output

Non-isolated, +12V, 1A, with electronic fuse protection

I/O Toolbox tools

The I/O Toolbox is a configurable interconnection of tools that generates events (usually triggers):

Line Input tool (LIN): edge detector delivering events on rising or falling edges of any selected input line.

Quadrature Decoder tool (QDC): a composite tool including:

A quadrature edge detector delivering events on selected transitions of selected pairs of input lines.

An optional backward motion compensator for clean line-scan image acquisition when the motion is unstable.

A 32-bit up/down counter for delivering a position value.

Device Link Trigger tool (DLT): delivers an event on reception of a valid high-speed CoaXPress 2.0 connection trigger packet message from the remote device.

User Actions Scheduler tool (UAS): to delegate the execution of 'User Actions' at a scheduled time or encoder position. Possible user actions include setting low/high/toggle any bit of the User Output Register or generation of any User Events.

Delay tool (DEL): to delay up to 16 events from one or two I/O toolbox event sources, by a programmable time or number of motion encoder ticks (any QDC events).

Divider tool (DIV): to generate an event every nth input events from any I/O toolbox event source.

Multiplier/divider tool (MDV): to generate m events every d input events from any I/O toolbox event source.

The 'Input Tools' (LIN, QDC, DLT and UAS) can be further processed by the 'Event Tools' (DEL, DIV and MDV) to generate any of the following "trigger" events:

The "cycle trigger" of the Camera and Illumination controller

The "cycle sequence trigger" of the Camera and Illumination controller

The "start-of-scan trigger" of the Acquisition Controller (line-scan only)

The "end-of-scan trigger" of the Acquisition Controller (line-scan only)

I/O Toolbox composition

8 LIN, 1 QDC, 2 DLT, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 2 C2C

C2C-Link

Description

Accurate synchronization of the trigger and the start-of-exposure of multiple grabber-controlled area-scan cameras.

Accurate synchronization of the start-of-cycle, start-of-scan and end-of-scan of multiple grabber-controlled line-scan cameras.

Specification

C2C-Link synchronizes cameras connected to:

- the same card

- to different cards in the same PC (requires an accessory cable such as the "3303 C2C-Link Ribbon Cable" or a custom-made C2C-Link cable)

- to different cards in different PCs (requires one "1636 InterPC C2C-Link Adapter" for each PC and one RJ 45 CAT 5 STP straight LAN cable for each adapter but the last one)

Maximum distance:

- 120 cm inside a PC

- 1200 m cumulated adapter to adapter cable length

Maximum trigger rate:

- 2.5 MHz for configurations using a single PC, or up to 10 PCs and 100 m total C2C-Link cable length

- 200 kHz for configurations up to 32 PCs and 1200m total C2C-Link cable length

Trigger propagation delay from master to slave devices:

- Less than 10 ns for cameras on the same card or on different cards in the same PC

- Less than 265 ns for cameras on different cards in different PCs (3 PCs and 40m total C2C-Link cable length)

Software

Host PC Operating System

Microsoft Windows 11, 10, 8.1, 7 for x86-64 (64-bit) processor architecture

Linux for x86-64 (64-bit) and AArch64 (64-bit) processor architectures

macOS for x86-64 (64-bit) and AArch64 (64-bit) processor architectures

APIs

EGrabber class, with C++ and .NET APIs: .NET assembly designed to be used with development environments compatible with .NET frameworks version 4.0 or higher

GenICam GenTL producer libraries compatible with C/C++ compilers:

- 'x86_64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of x86-64 (64-bit) applications

- 'aarch64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of AArch64 (64-bit) applications

Memento supported

Yes

Environmental conditions

Operating ambient air temperature

0 °C to +55 °C / +32 °F to +131 °F

Operating ambient air humidity

10% to 90% RH non-condensing

Storage ambient air temperature

-20 °C to +70 °C/ -4 °F to +158 °F

Storage ambient air humidity

10% to 90% RH non-condensing

Certifications

EMC standards

European Council EMC Directive 2014/30/EU

United States FCC rule 47 CFR 15

EMC - Emission

EN 55032:2015 / CISPR 32:2012 Class B

FCC 47 Part 15 Class B

EMC - Immunity

EN 55024:2010 / CISPR 24:2010

EN 55035:2017 / CISPR 35:2016

EN 61000-4-2:2009

EN 61000-4-3:2006

EN 61000-4-4:2004

EN 61000-4-6:2014

KC Certification

Korean Radio Waves Act, Article 58-2, Clause 3

Flammability

PCB compliant with UL 94 V-0

RoHS

European Union Directive [2015/863](#) (ROHS3)

REACH

European Union Regulation [1907/2006](#)

WEEE

Must be disposed of separately from normal household waste and must be recycled according to local regulations

Ordering Information

Product code - Description

PC3633 Coaxlink Quad CXP-12 3D-LLE

Related products

PC1625 DB25F I/O Adapter Cable

PC1636 InterPC C2C-Link Adapter

PC3303 C2C-Link Ribbon Cable

PC3304 HD26F I/O Adapter Cable

PC3610 HD26F I/O Extension Module - TTL-RS422

PC3612 HD26F I/O Extension Module - TTL-CMOS5V-RS422

Offices

- Europe, Middle East & Africa
Euresys SA
Contact support : support.europe@euresys.com

Sensor to Image GmbH
Contact support : support.europe@euresys.com
- China
Euresys Shanghai Liaison Office
Contact support : support.china@euresys.com

Euresys Shenzhen Liaison Office
Contact support : support.china@euresys.com
- Japan
Euresys Japan K.K.
Contact support : support.japan@euresys.com
- South Korea
Euresys South Korea Liaison Office
Contact support : support.korea@euresys.com
- Asia (other countries)
Euresys Pte. Ltd.
Contact support : support.asia@euresys.com
- North, Central & South America
Euresys Inc.
Contact support : support.usa@euresys.com

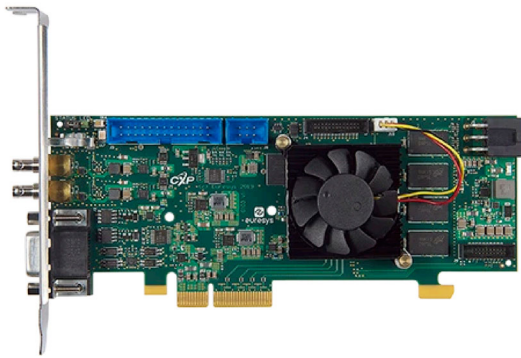
TKH Vision Experience Center
Contact support : support.usa@euresys.com

5/22/2024

Datasheet

Coaxlink Duo CXP-12

Two-connection CoaXPress CXP-12 frame grabber



- Two CoaXPress CXP-12 connections: 2,500 MB/s camera bandwidth
- PCIe 3.0 (Gen 3) x4 bus: 3,300 MB/s bus bandwidth
- Low-profile card. Delivered with standard and low-profile brackets
- Fan-cooled heatsink
- Feature-rich set of 10 digital I/O lines
- Extensive camera control functions
- Memento Event Logging Tool

Main benefits



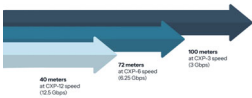
Low-profile PCIe card

Delivered with standard and low-profile brackets



PCIe 3.0 (Gen 3) x4 bus

- 3,300 MB/s sustained bus bandwidth
-



Long cable support

- 40 meters at CXP-12 speed (12.5 Gbps)
 - 72 meters at CXP-6 speed (6.25 Gbps)
 - 100 meters at CXP-3 speed (3 Gbps)
-



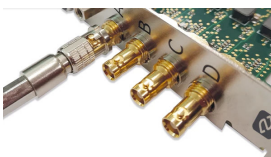
Power over CoaXPress

- Power over CoaXPress : Feed your camera up to 17 W per channel under 24 VDC with automatic device detection, measurement and overload protection.
 - Total and per-channel voltage and current measurement is possible, allowing validation and performance deviation monitoring.
-



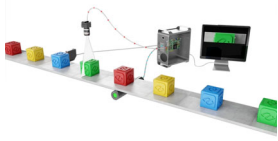
Use standard coaxial cables

- A single inexpensive cable for data transfer, camera control, trigger and power supply
 - Top reliability and flexibility, performs in the harshest environments
-



Micro-BNC (HD-BNC™) connectors for reliable connection

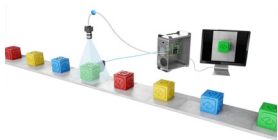
- Trusted push and turn, bayonet-style positive lock
- Allows for quick and easy connects and disconnects



Line-scan triggering capabilities

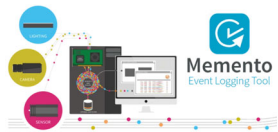
Euresys' frame grabbers offer many capabilities to synchronize line-scan or 1D cameras, sensors and lighting controllers. Frame grabbers can control the camera scanning rate based on the signals received from a motion encoder.

They support continuous web scanning (to inspect infinite, continuously moving surfaces without losing a single line) and discrete object scanning (to acquire the image of objects moving in front of the camera).



Area-scan triggering capabilities

Euresys' frame grabbers offer many capabilities to synchronize area-scan or 2D cameras, sensors and lighting controllers, for stationary or moving objects in the field of view, or moving cameras.



Memento Event Logging Tool

- Memento is an advanced development and debugging tool available for Coaxlink and Grablink cards.
- Memento records an accurate log of all the events related to the camera, the frame grabber and its driver as well as the application.
- It provides the developer with a precise timeline of time-stamped events, along with context information and logic analyzer view.
- It provides valuable assistance during application development and debugging, as well as during machine operation.



Compatible with eGrabber

- eGrabber Studio: eGrabber's new interactive evaluation and demonstration application
- GenICam Browser: An application giving access to the GenICam features exposed by the GenTL Producer(s)
- GenTL Console: A command-line tool giving access to the functions and commands exposed by the Euresys GenTL Producer



C2C-Link camera synchronization

Allows to accurately synchronize multiple area-scan or line-scan cameras connected

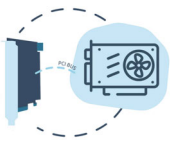
- to the same card
- to different cards in the same PC
- to different cards in different PCs



Compliant with GenICam

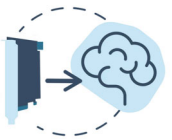
Including support for:

- GenApi
- The Standard Feature Naming Convention (SFNC)
- GenTL



Direct GPU transfer

- Sample programs for AMD DirectGMA and NVIDIA (CUDA) available.
- Direct GPU transfer eliminates unnecessary system memory copies, lowers CPU overhead, and reduces latency, resulting in significant performance improvements in data transfer times for applications.
- Direct capture of image data to GPU memory is available using AMD's DirectGMA. Compatible with AMD FirePro W5x00 and above and all AMD FirePro S series products.



High-performance DMA (Direct Memory Access)

- Direct transfer into user-allocated memory
- Hardware scatter-gather support

ARM **macOS**



Windows, Linux and macOS drivers available

Including support for Intel 64-bit platforms as well as ARM 64-bit platforms.

Other benefits

General purpose I/O lines

- Compatible with a wide range of sensors and motion encoders.
 - High-speed differential inputs: Quadrature motion encoder support up to 5 MHz.
 - Isolated current-sense inputs: 5V, 12V, 24V signaling voltages accepted, up to 50 kHz, individual galvanic isolation up to 250VDC and 170VAC RMS.
 - Isolated contact outputs.
 - High-speed 5V-compliant TTL inputs/ LVTTTL outputs.
-

Flexible line-scan camera operation with the rate converter

- The rate converter is a smart, programmable frequency multiplier/divider.
- Used with motion encoders and line-scan cameras, it allows the user to choose the aspect ratio of the pixels in the image.
- It provides a way to calibrate the acquisition chain to easily reach square (1:1 aspect ratio) pixels.

Specifications

Mechanical

Form factor

PCI Express card

Format

Low profile, half length, 4-lane PCI Express card

Cooling method

Air cooling, fan-cooled heatsink

Mounting

For insertion in a 4-lane or higher, PCI Express card slot.

Delivered with standard- and low-profile brackets for insertion in a standard- or a low-profile chassis.

Connectors

'A B' on card bracket:

- 2 x Micro-BNC 75 Ohms coaxial receptacles
- CoaXPress Host Interface

'EXTERNAL I/O' on card bracket:

- 15-pin 3-row high-density D-Sub female socket with UNC4-40 jack socket screws
- I/O lines and I/O power output

'INTERNAL I/O 1' on printed circuit board:

- 26-pin 2-row 0.1" pitch pin header with shrouding
- I/O lines and I/O power output

'I/O EXTENSION' on printed circuit board:

- 26-pin 2-row 0.05" pitch pin header with shrouding
- I/O extension cable socket

'C2C-LINK' on printed circuit board:

- 6-pin 2-row 0.1" pitch pin header with shrouding
- Card-to-card link

'AUXILIARY POWER INPUT' on printed circuit board:

- 6-pin PEG power socket
- 12 V DC power input for PoCXP and I/O power output

LED indicators

'A', 'B' on bracket:

- Bi-color red/green LEDs
- CoaXPress Host connector indicator

'FPGA STATUS LAMP' on PCB:

- Bi-color red/green LED
- FPGA status indicator

'BOARD STATUS LAMP' on PCB:

- Bi-color red/green LED
- Board status indicator

Switches

- 'RECOVERY' on PCB:
- 3-pin 1-row 0.1" header or 2-way DIP switch
 - Firmware emergency recovery

Dimensions

PCB L x H: 167.65 mm x 68.9 mm [6.6 in x 2.71 in]

Weight

- Net weight: 122 g [4.3 oz]
- Gross weight: 232 g [8.2 oz]

Host bus

Standard

PCI Express 3.0

Link width

- 4 lanes
- 1 lane or 2 lanes with reduced performance

Link speed

- 8.0 GT/s (PCIe 3.0)
- 5.0 GT/s (PCIe 2.0) with reduced performance

Maximum payload size

512 bytes

DMA

32- and 64-bit

Peak delivery bandwidth

3,900 MB/s

Effective (sustained) delivery bandwidth

3,350 MB/s (Host PC motherboard dependent)

Power consumption

Typ. 14.8 W (4.3W @ 3.3V, 10.5 W @12V), excluding camera and I/O power output

Camera / video inputs

Camera interface standard

CoaXPress

Interface standard(s)

CoaXPress 1.0, 1.1, 1.1.1, 2.0 and 2.1

Maximum link speed

CXP-12

Maximum link width

2 connections

Camera powering

PoCXP

Connectors

Two micro-BNC 75 Ohms (also known as HD-BNC™) CXP-12

Status LEDs

One CoaXPress Host connection status LED per connection

Number of cameras

Area-scan cameras:

- One 1- or 2-connection camera
- One or two 1-connection cameras

Line-scan cameras:

- One 1- or 2-connection camera
- One or two 1-connection cameras

Maximum number of cameras

2

Line-scan cameras supported

Yes

Maximum aggregated camera data transfer rate

25 Gbps (2,500 MB/s)

Supported CXP down-connection speeds

1.25 Gbps (CXP-1), 2.5 Gbps (CXP-2), 3.125 Gbps (CXP-3), 5 Gbps (CXP-5), 6.25 Gbps (CXP-6), 10.0 Gbps (CXP-10)*, and 12.5 Gbps (CXP-12)*

NOTE: mixing CXP-10 and CXP-12 is not allowed!

Supported CXP up-connection speeds

Low-speed 20.83... Mbps (CXP-1 to CXP-6)

Low-speed 41.66... Mbps (CXP-10, CXP-12)

Number of CXP data streams (per camera)

1 data stream per camera

Maximum CXP stream packet size

16,384 bytes

PoCXP (Power over CoaXPress)

PoCXP Safe Power:

17 W of 24V DC regulated power per CoaXPress connector

PoCXP Device detection and automatic power-on

Overload and short-circuit protections

On-board 12V to 24V DC/DC converter

A +12V power source must be connected to the AUXILIARY POWER INPUT connector using a 6-pin PEG cable

Camera types

Area-scan cameras:

Grayscale and color (RGB and Bayer CFA)

Single-tap (1X-1Y) progressive-scan

Line-scan cameras:

Grayscale and color RGB

Camera pixel formats supported

Mono8, Mono10, Mono12, Mono14, Mono16

BayerXX8, BayerXX10, BayerXX12, BayerXX14, BayerXX16 where XX = GR, RG, GB, or BG

RGB8, RGB10, RGB12, RGB14, RGB16

RGBA8, RGBA10, RGBA12, RGBA14, RGBA16

YCbCr601_422_8, YCbCr601_422_10

YCbCr709_422_8, YCbCr709_422_10

YUV422_8, YUV422_10

Raw

Area-scan camera control

Trigger

Precise control of asynchronous reset cameras, with exposure control.

Support of camera exposure/readout overlap.

Support of external hardware trigger, with optional delay and trigger decimation.

Strobe

Accurate control of the strobe position for strobed light sources.

Support of early and late strobe pulses.

Line-scan camera control

Scan/page trigger

Precise control of start-of-scan and end-of-scan triggers.

Support of external hardware trigger, with optional delay.

Support of infinite acquisition, without missing line, for web inspection applications.

Line trigger

Support for quadrature motion encoders, with programmable noise filters, selection of acquisition direction and backward motion compensation.

Rate Converter tool for fine control of the pixel aspect ratio: Rate Conversion Ratio in the range 0.001 to 1000 with an accuracy better than 0.1%.

Rate Divider tool

Line strobe

Accurate control of the strobe position for strobed light sources.

On-board processing

On-board memory

1 GB

Image data stream processing

Unpacking of 10-/12-/14-bit to 16-bit with selectable justification to LSB or MSb

Optional swap of R and B components

Little endian conversion

Input LUT (Lookup Table)

Monochrome 8-bit to 8-bit transformation

Monochrome 10-bit to 8-, 10- or 16-bit transformations

Monochrome 12-bit to 8-, 12- or 16-bit transformations

Bayer CFA to RGB decoder

5x5 gradient-based interpolation method on '1-camera' firmware variant

Data stream statistics

Measurement of:

Frame rate (Area-scan only)

Line rate

Data rate

Configurable averaging interval

Event signaling and counting

The application software can be notified of the occurrence of various events:

Standard event: the EVENT_NEW_BUFFER event notifies the application of newly filled buffers

A large set of custom events

Custom events sources:

I/O Toolbox events

Camera and Illumination control events

CoaXPress data stream events

CoaXPress host interface events

Each custom event is associated with a 32-bit counter that counts the number of occurrences

The last three 32-bit context data words of the event context data can be configured with event-specific context data:

Event-specific data

State of all System I/O lines sampled at the event occurrence time

Value of any event counter

General Purpose Inputs and Outputs

Number of lines

10 I/O lines:

2 differential inputs (DIN)

2 singled-ended TTL inputs/outputs (TTLIO)

4 isolated inputs (IIN)

2 isolated outputs (IOUT)

NOTES:

Only 2 IIN and 1 IOOUT lines are available on the EXTERNAL I/O connector.

The number of I/O lines can be extended using I/O modules attached to the I/O EXTENSION connector.

Usage

Any I/O input lines can be used by any LIN tool of the I/O Toolbox

Selected pairs of I/O input lines can be used by any QDC tool of the I/O toolbox to decode A/B signals of a motion encoder

Electrical specifications

DIN: High-speed differential inputs, up to 5 MHz, compatible with ANSI/EIA/TIA-422/485 differential line drivers and complementary TTL drivers

TTLIO: High-speed 5V-compliant TTL inputs or LVTTTL outputs, compatible with totem-pole LVTTTL, TTL, 5V CMOS drivers or LVTTTL, TTL, 3V CMOS receivers

IIN: Isolated current-sense inputs with wide voltage input range up to 30V, compatible with totem-pole LVTTTL, TTL, 5V CMOS drivers, RS-422 differential line drivers, potential free contacts, solid-state relays and opto-couplers

IOOUT: Isolated contact outputs compatible with 30V / 100mA loads

NOTE: IIN and IOOUT lines provide a functional isolation grade for the circuit technical protection. It does not provide an isolation that can protect a human being from electrical shock!

Filter control

Glitch removal filter available on all System I/O input lines

Configurable filter delay:

Custom value

Fixed values for DIN and TTLIO lines: 50 ns, 100 ns, 200 ns, 500 ns, 1 μ s

Fixed values for IIN lines: 500 ns, 1 μ s, 2 μ s, 5 μ s, 10 μ s

Polarity control

Yes

Power output

Non-isolated, +12V, 1A, with electronic fuse protection

I/O Toolbox tools

The I/O Toolbox is a configurable interconnection of tools that generates events (usually triggers):

Line Input tool (LIN): edge detector delivering events on rising or falling edges of any selected input line.

Quadrature Decoder tool (QDC): a composite tool including:

A quadrature edge detector delivering events on selected transitions of selected pairs of input lines.

An optional backward motion compensator for clean line-scan image acquisition when the motion is unstable.

A 32-bit up/down counter for delivering a position value.

Device Link Trigger tool (DLT): delivers an event on reception of a valid high-speed CoaXPress 2.0 connection trigger packet message from the remote device.

User Actions Scheduler tool (UAS): to delegate the execution of 'User Actions' at a scheduled time or encoder position. Possible user actions include setting low/high/toggle any bit of the User Output Register or generation of any User Events.

Delay tool (DEL): to delay up to 16 events from one or two I/O toolbox event sources, by a programmable time or number of motion encoder ticks (any QDC events).

Divider tool (DIV): to generate an event every nth input events from any I/O toolbox event source.

Multiplier/divider tool (MDV): to generate m events every d input events from any I/O toolbox event source.

The 'Input Tools' (LIN, QDC, DLT and UAS) can be further processed by the 'Event Tools' (DEL, DIV and MDV) to generate any of the following "trigger" events:

- The "cycle trigger" of the Camera and Illumination controller
- The "cycle sequence trigger" of the Camera and Illumination controller
- The "start-of-scan trigger" of the Acquisition Controller (line-scan only)
- The "end-of-scan trigger" of the Acquisition Controller (line-scan only)

I/O Toolbox composition

Determined by the selected firmware variant:

'1-camera': 8 LIN, 1 QDC, 2 DLT, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 2 C2C

'2-camera': 8 LIN, 2 QDC, 4 DLT, 1 UAS, 2 DEL, 2 DIV, 2 MDV, 2 C2C

'1-camera, line-scan': 8 LIN, 1 QDC, 2 DLT, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 3 C2C

'2-camera, line-scan': 8 LIN, 2 QDC, 4 DLT, 1 UAS, 2 DEL, 2 DIV, 2 MDV, 3 C2C

C2C-Link

Description

Accurate synchronization of the trigger and the start-of-exposure of multiple grabber-controlled area-scan cameras.

Accurate synchronization of the start-of-cycle, start-of-scan and end-of-scan of multiple grabber-controlled line-scan cameras.

Specification

C2C-Link synchronizes cameras connected to:

- the same card
- to different cards in the same PC (requires an accessory cable such as the "3303 C2C-Link Ribbon Cable" or a custom-made C2C-Link cable)
- to different cards in different PCs (requires one "1636 InterPC C2C-Link Adapter" for each PC and one RJ 45 CAT 5 STP straight LAN cable for each adapter but the last one)

Maximum distance:

- 120 cm inside a PC
- 1200 m cumulated adapter to adapter cable length

Maximum trigger rate:

- 2.5 MHz for configurations using a single PC, or up to 10 PCs and 100 m total C2C-Link cable length
- 200 kHz for configurations up to 32 PCs and 1200m total C2C-Link cable length

Trigger propagation delay from master to slave devices:

Less than 10 ns for cameras on the same card or on different cards in the same PC

Less than 265 ns for cameras on different cards in different PCs (3 PCs and 40m total C2C-Link cable length)

Software

Host PC Operating System

Microsoft Windows 11, 10 for x86-64 (64-bit) processor architecture

Linux for x86-64 (64-bit) and AArch64 (64-bit) processor architectures

macOS for x86-64 (64-bit) and AArch64 (64-bit) processor architectures

APIs

EGrabber class, with C++ and .NET APIs: .NET assembly designed to be used with development environments compatible with .NET frameworks version 4.0 or higher

GenICam GenTL producer libraries compatible with C/C++ compilers:

'x86_64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of x86-64 (64-bit) applications

'aarch64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of AArch64 (64-bit) applications

Memento supported

Yes

Environmental conditions

Operating ambient air temperature

0 °C to +55 °C / +32 °F to +131 °F

Operating ambient air humidity

10% to 90% RH non-condensing

Storage ambient air temperature

-20 °C to +70 °C/ -4 °F to +158 °F

Storage ambient air humidity

10% to 90% RH non-condensing

Certifications

EMC standards

European Council EMC Directive 2014/30/EU

United States FCC rule 47 CFR 15

EMC - Emission

EN 55032:2015 / CISPR 32:2012 Class B

FCC 47 Part 15 Class B

EMC - Immunity

EN 55024:2010 / CISPR 24:2010

EN 55035:2017 / CISPR 35:2016

EN 61000-4-2:2009

EN 61000-4-3:2006

EN 61000-4-4:2004

EN 61000-4-6:2014

KC Certification

Korean Radio Waves Act, Article 58-2, Clause 3

Flammability

PCB compliant with UL 94 V-0

RoHS

European Union Directive [2015/863](#) (ROHS3)

REACH

European Union Regulation [1907/2006](#)

WEEE

Must be disposed of separately from normal household waste and must be recycled according to local regulations

Ordering Information

Product code - Description

PC3622 Coaxlink Duo CXP-12

Related products

PC1625 DB25F I/O Adapter Cable

PC1636 InterPC C2C-Link Adapter

PC3303 C2C-Link Ribbon Cable

PC3610 HD26F I/O Extension Module - TTL-RS422

PC3612 HD26F I/O Extension Module - TTL-CMOS5V-RS422

PC3614 HD26F I/O Extension Module - Standard I/O Set

Offices

- Europe, Middle East & Africa
Euresys SA
Contact support : support.europe@euresys.com

Sensor to Image GmbH
Contact support : support.europe@euresys.com
- China
Euresys Shanghai Liaison Office
Contact support : support.china@euresys.com

Euresys Shenzhen Liaison Office
Contact support : support.china@euresys.com
- Japan
Euresys Japan K.K.
Contact support : support.japan@euresys.com
- South Korea
Euresys South Korea Liaison Office
Contact support : support.korea@euresys.com
- Asia (other countries)
Euresys Pte. Ltd.
Contact support : support.asia@euresys.com
- North, Central & South America
Euresys Inc.
Contact support : support.usa@euresys.com

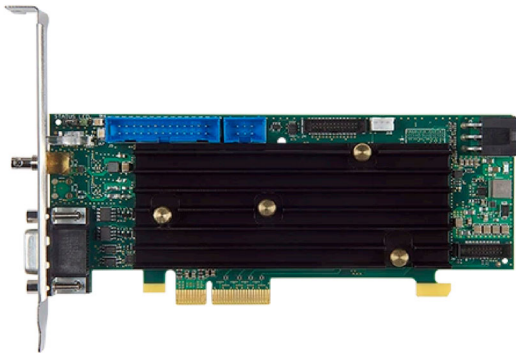
TKH Vision Experience Center
Contact support : support.usa@euresys.com

5/22/2024

Datasheet

Coaxlink Mono CXP-12 LH

One-connection CoaXPress CXP-12 frame grabber



- One CoaXPress CXP-12 connection: 1,250 MB/s camera bandwidth
- PCIe 3.0 (Gen 3) x4 bus: 3,300 MB/s bus bandwidth
- Low-profile card. Delivered with standard and low-profile brackets
- Passive (fanless) heatsink
- Feature-rich set of 10 digital I/O lines
- Extensive camera control functions
- Memento Event Logging Tool

Main benefits



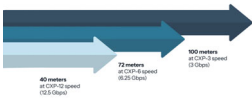
Low-profile PCIe card

Delivered with standard and low-profile brackets



PCIe 3.0 (Gen 3) x4 bus

- 3,300 MB/s sustained bus bandwidth



Long cable support

- 40 meters at CXP-12 speed (12.5 Gbps)
- 72 meters at CXP-6 speed (6.25 Gbps)
- 100 meters at CXP-3 speed (3 Gbps)



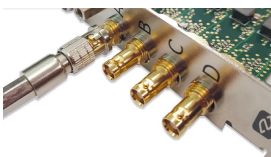
Power over CoaXPress

- Power over CoaXPress : Feed your camera up to 17 W per channel under 24 VDC with automatic device detection, measurement and overload protection.
- Total and per-channel voltage and current measurement is possible, allowing validation and performance deviation monitoring.



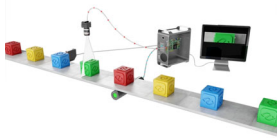
Use standard coaxial cables

- A single inexpensive cable for data transfer, camera control, trigger and power supply
- Top reliability and flexibility, performs in the harshest environments



Micro-BNC (HD-BNC™) connectors for reliable connection

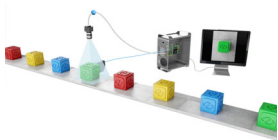
- Trusted push and turn, bayonet-style positive lock
- Allows for quick and easy connects and disconnects



Line-scan triggering capabilities

Euresys' frame grabbers offer many capabilities to synchronize line-scan or 1D cameras, sensors and lighting controllers. Frame grabbers can control the camera scanning rate based on the signals received from a motion encoder.

They support continuous web scanning (to inspect infinite, continuously moving surfaces without losing a single line) and discrete object scanning (to acquire the image of objects moving in front of the camera).



Area-scan triggering capabilities

Euresys' frame grabbers offer many capabilities to synchronize area-scan or 2D cameras, sensors and lighting controllers, for stationary or moving objects in the field of view, or moving cameras.



Memento Event Logging Tool

- Memento is an advanced development and debugging tool available for Coaxlink and Grablink cards.
- Memento records an accurate log of all the events related to the camera, the frame grabber and its driver as well as the application.
- It provides the developer with a precise timeline of time-stamped events, along with context information and logic analyzer view.
- It provides valuable assistance during application development and debugging, as well as during machine operation.



Compatible with eGrabber

- eGrabber Studio: eGrabber's new interactive evaluation and demonstration application
- GenICam Browser: An application giving access to the GenICam features exposed by the GenTL Producer(s)
- GenTL Console: A command-line tool giving access to the functions and commands exposed by the Euresys GenTL Producer



C2C-Link camera synchronization

Allows to accurately synchronize multiple area-scan or line-scan cameras connected

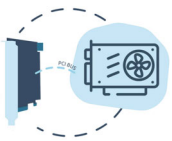
- to the same card
- to different cards in the same PC
- to different cards in different PCs



Compliant with GenICam

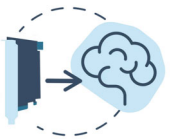
Including support for:

- GenApi
- The Standard Feature Naming Convention (SFNC)
- GenTL



Direct GPU transfer

- Sample programs for AMD DirectGMA and NVIDIA (CUDA) available.
- Direct GPU transfer eliminates unnecessary system memory copies, lowers CPU overhead, and reduces latency, resulting in significant performance improvements in data transfer times for applications.
- Direct capture of image data to GPU memory is available using AMD's DirectGMA. Compatible with AMD FirePro W5x00 and above and all AMD FirePro S series products.



High-performance DMA (Direct Memory Access)

- Direct transfer into user-allocated memory
- Hardware scatter-gather support

ARM **macOS**

Windows, Linux and macOS drivers available



Including support for Intel 64-bit platforms as well as ARM 64-bit platforms.

Other benefits

General purpose I/O lines

- Compatible with a wide range of sensors and motion encoders.
- High-speed differential inputs: Quadrature motion encoder support up to 5 MHz.
- Isolated current-sense inputs: 5V, 12V, 24V signaling voltages accepted, up to 50 kHz, individual galvanic isolation up to 250VDC and 170VAC RMS.
- Isolated contact outputs.
- High-speed 5V-compliant TTL inputs/ LVTTTL outputs.

Specifications

Mechanical

Form factor

PCI Express card

Format

Low profile, half length, 4-lane PCI Express card

Cooling method

Air-cooling, fanless

Mounting

For insertion in a 4-lane or higher, PCI Express card slot.

Delivered with standard- and low-profile brackets for insertion in a standard- or a low-profile chassis.

Connectors

'A' on card bracket:

Micro-BNC 75 Ohms coaxial receptacle

CoaXPress Host Interface

'EXTERNAL I/O' on card bracket:

15-pin 3-row high-density D-Sub female socket with UNC4-40 jack socket screws

I/O lines and I/O power output

'INTERNAL I/O 1' on printed circuit board:

26-pin 2-row 0.1" pitch pin header with shrouding

I/O lines and I/O power output

'I/O EXTENSION' on printed circuit board:

26-pin 2-row 0.05" pitch pin header with shrouding

I/O extension cable socket

'C2C-LINK' on printed circuit board:

6-pin 2-row 0.1" pitch pin header with shrouding

Card-to-card link

'AUXILIARY POWER INPUT' on printed circuit board:

6-pin PEG power socket

12 V DC power input for PoCXP and I/O power output

LED indicators

'A' on bracket:

- Bi-color red/green LED
- CoaXPress Host connector indicator

'FPGA STATUS LAMP' on PCB:

- Bi-color red/green LED
- FPGA status indicator

'BOARD STATUS LAMP' on PCB:

- Bi-color red/green LED
- Board status indicator

Switches

- 'RECOVERY' on PCB:
- 3-pin 1-row 0.1" header or 2-way DIP switch
 - Firmware emergency recovery

Dimensions

PCB L x H: 167.65 mm x 68.9 mm [6.6 in x 2.71 in]

Weight

- Net weight: 159 g [5.6 oz]
- Gross weight: 275 g [9.7 oz]

Host bus

Standard

PCI Express 3.0

Link width

- 4 lanes
- 1 lane or 2 lanes with reduced performance

Link speed

- 8.0 GT/s (PCIe 3.0)
- 5.0 GT/s (PCIe 2.0) with reduced performance

Maximum payload size

512 bytes

DMA

32- and 64-bit

Peak delivery bandwidth

3,900 MB/s

Effective (sustained) delivery bandwidth

3,350 MB/s (Host PC motherboard dependent)

Power consumption

Typ. 11.5 W (3 W @ 3.3 V, 8.5 W @ 12 V), excluding camera and I/O power output

Camera / video inputs

Camera interface standard

CoaXPress

Interface standard(s)

CoaXPress 1.0, 1.1, 1.1.1, 2.0 and 2.1

Maximum link speed

CXP-12

Maximum link width

1 connection

Camera powering

PoCXP

Connectors

One micro-BNC 75 Ohms (also known as HD-BNC™) CXP-12

Status LEDs

One CoaXPress Host connection status LED per connection

Number of cameras

Area-scan cameras:

One 1-connection camera

Line-scan cameras:

One 1-connection camera

Maximum number of cameras

1

Line-scan cameras supported

Yes

Maximum aggregated camera data transfer rate

12.5 Gbps (1,250 MB/s)

Supported CXP down-connection speeds

1.25 Gbps (CXP-1), 2.5 Gbps (CXP-2), 3.125 Gbps (CXP-3), 5 Gbps (CXP-5), 6.25 Gbps (CXP-6), 10.0 Gbps (CXP-10), and 12.5 Gbps (CXP-12)

Supported CXP up-connection speeds

Low-speed 20.83... Mbps (CXP-1 to CXP-6)

Low-speed 41.66... Mbps (CXP-10, CXP-12)

Number of CXP data streams (per camera)

1 data stream per camera

Maximum CXP stream packet size

16,384 bytes

PoCXP (Power over CoaXPress)

PoCXP Safe Power:

- 25 W of 24V DC regulated power

- PoCXP Device detection and automatic power-on

- Overload and short-circuit protections

On-board 12V to 24V DC/DC converter

A +12V power source must be connected to the AUXILIARY POWER INPUT connector using a 6-pin PEG cable

Camera types

Area-scan cameras:

- Grayscale and color (RGB and Bayer CFA)

- Single-tap (1X-1Y) progressive-scan

Line-scan cameras:

- Grayscale and color RGB

Camera pixel formats supported

Mono8, Mono10, Mono12, Mono14, Mono16

BayerXX8, BayerXX10, BayerXX12, BayerXX14, BayerXX16 where XX = GR, RG, GB, or BG

RGB8, RGB10, RGB12, RGB14, RGB16

RGBA8, RGBA10, RGBA12, RGBA14, RGBA16

YCbCr601_422_8, YCbCr601_422_10

YCbCr709_422_8, YCbCr709_422_10

YUV422_8, YUV422_10

Raw

Area-scan camera control

Trigger

Precise control of asynchronous reset cameras, with exposure control.
Support of camera exposure/readout overlap.
Support of external hardware trigger, with optional delay and trigger decimation.

Strobe

Accurate control of the strobe position for strobed light sources.
Support of early and late strobe pulses.

Line-scan camera control

Scan/page trigger

Precise control of start-of-scan and end-of-scan triggers.
Support of external hardware trigger, with optional delay.
Support of infinite acquisition, without missing line, for web inspection applications.

Line trigger

Support for quadrature motion encoders, with programmable noise filters, selection of acquisition direction and backward motion compensation.
Rate Converter tool for fine control of the pixel aspect ratio: Rate Conversion Ratio in the range 0.001 to 1000 with an accuracy better than 0.1%.
Rate Divider tool

Line strobe

Accurate control of the strobe position for strobed light sources.

On-board processing

On-board memory

512 MB

Image data stream processing

Unpacking of 10-/12-/14-bit to 16-bit with selectable justification to LSb or MSb
Optional swap of R and B components
Little endian conversion

Input LUT (Lookup Table)

Monochrome 8-bit to 8-bit transformation
Monochrome 10-bit to 8-, 10- or 16-bit transformations

Monochrome 12-bit to 8-, 12- or 16-bit transformations

Data stream statistics

Measurement of:

- Frame rate (Area-scan only)
- Line rate
- Data rate

Configurable averaging interval

Event signaling and counting

The application software can be notified of the occurrence of various events:

- Standard event: the EVENT_NEW_BUFFER event notifies the application of newly filled buffers
- A large set of custom events

Custom events sources:

- I/O Toolbox events
- Camera and Illumination control events
- CoaXPress data stream events
- CoaXPress host interface events

Each custom event is associated with a 32-bit counter that counts the number of occurrences

The last three 32-bit context data words of the event context data can be configured with event-specific context data:

- Event-specific data
- State of all System I/O lines sampled at the event occurrence time
- Value of any event counter

General Purpose Inputs and Outputs

Number of lines

10 I/O lines:

- 2 differential inputs (DIN)
- 2 singled-ended TTL inputs/outputs (TTLIO)
- 4 isolated inputs (IIN)
- 2 isolated outputs (IOUT)

NOTES:

Only 2 IIN and 1 IOUT lines are available on the EXTERNAL I/O connector.

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Usage

Any I/O input lines can be used by any LIN tool of the I/O Toolbox

Selected pairs of I/O input lines can be used by any QDC tool of the I/O toolbox to decode A/B signals of a motion encoder

Electrical specifications

DIN: High-speed differential inputs, up to 5 MHz, compatible with ANSI/EIA/TIA-422/485 differential line drivers and complementary TTL drivers

TTLIO: High-speed 5V-compliant TTL inputs or LVTTTL outputs, compatible with totem-pole LVTTTL, TTL, 5V CMOS drivers or LVTTTL, TTL, 3V CMOS receivers

IIN: Isolated current-sense inputs with wide voltage input range up to 30V, compatible with totem-pole LVTTTL, TTL, 5V CMOS drivers, RS-422 differential line drivers, potential free contacts, solid-state relays and opto-couplers

IOUT: Isolated contact outputs compatible with 30V / 100mA loads

NOTE: IIN and IOUT lines provide a functional isolation grade for the circuit technical protection. It does not provide an isolation that can protect a human being from electrical shock!

Filter control

Glitch removal filter available on all System I/O input lines

Configurable filter delay:

Custom value

Fixed values for DIN and TTLIO lines: 50 ns, 100 ns, 200 ns, 500 ns, 1 μ s

Fixed values for IIN lines: 500 ns, 1 μ s, 2 μ s, 5 μ s, 10 μ s

Polarity control

Yes

Power output

Non-isolated, +12V, 1A, with electronic fuse protection

I/O Toolbox tools

The I/O Toolbox is a configurable interconnection of tools that generates events (usually triggers):

Line Input tool (LIN): edge detector delivering events on rising or falling edges of any selected input line.

Quadrature Decoder tool (QDC): a composite tool including:

A quadrature edge detector delivering events on selected transitions of selected pairs of input lines.

An optional backward motion compensator for clean line-scan image acquisition when the motion is unstable.

A 32-bit up/down counter for delivering a position value.

Device Link Trigger tool (DLT): delivers an event on reception of a valid high-speed CoaXPress 2.0 connection trigger packet message from the remote device.

User Actions Scheduler tool (UAS): to delegate the execution of 'User Actions' at a scheduled time or encoder position. Possible user actions include setting low/high/toggle any bit of the User Output Register or generation of any User Events.

Delay tool (DEL): to delay up to 16 events from one or two I/O toolbox event sources, by a programmable time or number of motion encoder ticks (any QDC events).

Divider tool (DIV): to generate an event every nth input events from any I/O toolbox event source.

Multiplier/divider tool (MDV): to generate m events every d input events from any I/O toolbox event source.

The 'Input Tools' (LIN, QDC, DLT and UAS) can be further processed by the 'Event Tools' (DEL, DIV and MDV) to generate any of the following "trigger" events:

- The "cycle trigger" of the Camera and Illumination controller
- The "cycle sequence trigger" of the Camera and Illumination controller
- The "start-of-scan trigger" of the Acquisition Controller (line-scan only)
- The "end-of-scan trigger" of the Acquisition Controller (line-scan only)

I/O Toolbox composition

Determined by the selected firmware variant:

'1-camera': 8 LIN, 1 QDC, 2 DLT, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 2 C2C

'1-camera, line-scan': 8 LIN, 1 QDC, 2 DLT, 1 UAS, 2 DEL, 1 DIV, 1 MDV, 3 C2C

C2C-Link

Description

Accurate synchronization of the trigger and the start-of-exposure of multiple grabber-controlled area-scan cameras.

Accurate synchronization of the start-of-cycle, start-of-scan and end-of-scan of multiple grabber-controlled line-scan cameras.

Specification

C2C-Link synchronizes cameras connected to:

- the same card
- to different cards in the same PC (requires an accessory cable such as the "3303 C2C-Link Ribbon Cable" or a custom-made C2C-Link cable)
- to different cards in different PCs (requires one "1636 InterPC C2C-Link Adapter" for each PC and one RJ 45 CAT 5 STP straight LAN cable for each adapter but the last one)

Maximum distance:

- 120 cm inside a PC
- 1200 m cumulated adapter to adapter cable length

Maximum trigger rate:

- 2.5 MHz for configurations using a single PC, or up to 10 PCs and 100 m total C2C-Link cable length
- 200 kHz for configurations up to 32 PCs and 1200m total C2C-Link cable length

Trigger propagation delay from master to slave devices:

- Less than 10 ns for cameras on the same card or on different cards in the same PC
- Less than 265 ns for cameras on different cards in different PCs (3 PCs and 40m total C2C-Link cable length)

Software

Host PC Operating System

- Microsoft Windows 11, 10 for x86-64 (64-bit) processor architecture
- Linux for x86-64 (64-bit) and AArch64 (64-bit) processor architectures
- macOS for x86-64 (64-bit) and AArch64 (64-bit) processor architectures

APIs

EGrabber class, with C++ and .NET APIs: .NET assembly designed to be used with development environments compatible with .NET frameworks version 4.0 or higher

GenICam GenTL producer libraries compatible with C/C++ compilers:

'x86_64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of x86-64 (64-bit) applications

'aarch64' dynamic library designed to be used with ISO-compliant C/C++ compilers for the development of AArch64 (64-bit) applications

Memento supported

Yes

Environmental conditions

Operating ambient air temperature

0 °C to +55 °C / +32 °F to +131 °F, with minimum 150 LFM (Linear Feet per Minute) required airflow

Operating ambient air humidity

10% to 90% RH non-condensing

Storage ambient air temperature

-20 °C to +70 °C / -4 °F to +158 °F

Storage ambient air humidity

10% to 90% RH non-condensing

Certifications

EMC standards

European Council EMC Directive 2014/30/EU

United States FCC rule 47 CFR 15

EMC - Emission

EN 55032:2015 / CISPR 32:2012 Class B

FCC 47 Part 15 Class B

EMC - Immunity

EN 55024:2010 / CISPR 24:2010

EN 55035:2017 / CISPR 35:2016

EN 61000-4-2:2009

EN 61000-4-3:2006

EN 61000-4-4:2004

EN 61000-4-6:2014

KC Certification

Korean Radio Waves Act, Article 58-2, Clause 3

Flammability

PCB compliant with UL 94 V-0

RoHS

European Union Directive [2015/863](#) (ROHS3)

REACH

European Union Regulation [1907/2006](#)

WEEE

Must be disposed of separately from normal household waste and must be recycled according to local regulations

Ordering Information

Product code - Description

PC3621-LH Coaxlink Mono CXP-12 LH

Related products

PC1625 DB25F I/O Adapter Cable

PC1636 InterPC C2C-Link Adapter

PC3303 C2C-Link Ribbon Cable

PC3610 HD26F I/O Extension Module - TTL-RS422

PC3612 HD26F I/O Extension Module - TTL-CMOS5V-RS422

PC3614 HD26F I/O Extension Module - Standard I/O Set

PC3618 HD26F I/O Extension Module - Fast I/O

Offices

- Europe, Middle East & Africa
Euresys SA
Contact support : support.europe@euresys.com

Sensor to Image GmbH
Contact support : support.europe@euresys.com
- China
Euresys Shanghai Liaison Office
Contact support : support.china@euresys.com

Euresys Shenzhen Liaison Office
Contact support : support.china@euresys.com
- Japan
Euresys Japan K.K.
Contact support : support.japan@euresys.com
- South Korea
Euresys South Korea Liaison Office
Contact support : support.korea@euresys.com
- Asia (other countries)
Euresys Pte. Ltd.
Contact support : support.asia@euresys.com
- North, Central & South America
Euresys Inc.
Contact support : support.usa@euresys.com

TKH Vision Experience Center
Contact support : support.usa@euresys.com

5/22/2024

Datasheet

Coaxlink CXP-12 to QSFP+ Converter

Four-connection CoaXPress CXP-12 to CoaXPress-over-Fiber converter



- Provides easy cable length extension using CoaXPress-over-Fiber
- Allows connecting a CXP-12 camera to a Coaxlink QSFP+ frame grabber
- Four CoaXPress CXP-12 connections on the camera side
- One QSFP+ port compliant with 40 Gbps optical modules on the frame grabber side
- 5,000 MB/s camera bandwidth
- PoCXP camera power supply

Main benefits



Acquire images from the fastest and highest resolution cameras

- Highest data acquisition rate in the industry
 - Up to 5,000 MB/s bandwidth from camera to host PC memory
-



CoaXPRESS-over-Fiber is a light but significant extension of the existing CoaXPRESS specification

CoaXPRESS (CXP) is the de-facto standard for high-bandwidth computer vision applications. CoaXPRESS 2.1, the latest version of the specification, specifies the CXP-12 speed, a 12.5 Gbps (Gigabit per second) connection over a coaxial copper cable.

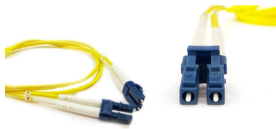
As link aggregation is common with CoaXPRESS, bandwidths of 50 Gbps (12.5 x 4) are easily achievable with four CXP-12 connections. CoaXPRESS-over-Fiber has been designed as an add-on to the CoaXPRESS specification. It provides a way to run the CoaXPRESS protocol, as it is, unmodified, over a standard Ethernet connection, including fiber optics. As such, CoaXPRESS-over-Fiber uses standard electronics, connectors and cables designed for Ethernet, but the protocol is CoaXPRESS, not Ethernet, not GigE Vision.



What are the cable options for CoaXPRESS-over-Fiber?

One of the most important benefits of CoaXPRESS-over-Fiber is the wide variety of connectivity options already available from multiple companies. The initial connectivity options for CoaXPRESS-over-Fiber and the Coaxlink QSFP+ at 10 Gbps are SFP+ and QSFP+ (Quad, or four times SFP+) modules.

The advantage of using modules compared to fixed interfaces is that ports can be equipped with any suitable type of transceiver as required by the application. A variety of transmitter and receiver types is available, allowing users to select the appropriate transceiver to provide the required optical reach over multi-mode or single-mode fiber.



What is the maximum cable length with single-mode fibers?

With a standard 40GBASE-ER4 QSFP+ LC DOM Optical Transceiver Module and an LC-Duplex single-mode fiber cable, the maximum cable length is 40 kilometers. This solution is suitable, for example, for video transmission applications.



What is the maximum cable length with multi-mode fibers?

With a standard 40GBASE-SR4 QSFP+ Optical Transceiver Module and an MTP/MPO multi-mode fiber cable, the maximum cable length is 150 meters. This solution is suitable for machine vision applications.



Power over CoaXPress

- Power over CoaXPress : Feed your camera up to 17 W per channel under 24 VDC with automatic device detection, measurement and overload protection.
- Total and per-channel voltage and current measurement is possible, allowing validation and performance deviation monitoring.



Compatible with eGrabber

- eGrabber Studio: eGrabber's new interactive evaluation and demonstration application
- GenICam Browser: An application giving access to the GenICam features exposed by the GenTL Producer(s)
- GenTL Console: A command-line tool giving access to the functions and commands exposed by the Euresys GenTL Producer



Compliant with GenICam

Including support for:

- GenApi
- The Standard Feature Naming Convention (SFNC)
- GenTL

Other benefits

What are the pros and cons of using fiber optics?

Pros

- First and foremost, cable length is not an issue anymore as fiber connectivity is basically not limited in length.
- Fiber optics provide more bandwidth, as connectivity at 10 and 25 Gbps per fiber is standard today and widely used in data centers.
- Fiber optics are immune to electrical noise, which will be a significant advantage on the production floor and in some medical applications.
- Fiber optics are lighter and smaller in size than the equivalent copper cabling, making it appropriate for applications where this characteristic is essential, like in aircrafts or vehicles.

Cons

- There is no “power over fiber”. As signals in fiber optics are transmitted using light, there is no way to transfer power over fiber optics and devices such as cameras must be powered separately.
-

What are the benefits of using CoaXPress-over-Fiber for my application?

- Available as CXP to nGMII (device) or nGMII to CXP (host) Bridge IP Cores
- Ultra-high data/frame rates
- Many accessory and cabling options to cover any length requirement
- Low CPU overhead, low latency, low jitter image acquisition
- Highest camera count per PC performance
- Very competitive cost/performance ratio
- Wide industry acceptance due to J11A standardization
- Ready for CXP25

What are the jitter and latency of CoaXPress-over-Fiber? How do they compare to "traditional" CoaXPress?

CoaXPress-over-Fiber is based on the CoaXPress protocol and it exhibits the same high performance as CoaXPress in terms of jitter and latency. In addition, as CoaXPress-over-Fiber supports higher transmission speed compared to CoaXPress, the jitter and latency will be further improved in these versions.

Specifications

Mechanical

Form factor

Stand-alone device

Cooling method

Air cooling, fan-cooled heatsink

Housing

Aluminum housing

Mounting

Desktop

Connectors

'COAXPRESS A B C D (INPUT)' on front panel:

- 4 x Micro-BNC 75 Ohms coaxial receptacles

- CoaXPress Host Interface

'QSFP+ (OUTPUT)' on rear panel:

- Enhanced Quad Small Form-factor Pluggable port

- CoaXPress-over-Fiber Device Interface

'POWER' on rear panel:

- 2-pin 5.08mm pitch terminal socket

- 24 V DC power input

LED indicators

'A', 'B', 'C', 'D' on front panel:

- Bi-color red/green LEDs

- CoaXPress Host connector indicator

'A', 'B', 'C', 'D' on rear panel:

- Bi-color red/green LEDs

- CoaXPress-over-Fiber Device connector indicator

'STATUS' on rear panel:

- Bi-color red/green LED

- FPGA status indicator

'POWER' on rear panel:

- Bi-color red/green LED

Board status indicator

Dimensions

L x W x H: 208 mm x 113 mm x 56.5 mm [8.2 in x 4.45 in x 2.22 in]

Weight

Net weight: 776 g [27.4 oz]

Gross weight: 930 g [32.8 oz]

Camera / video inputs

Camera interface standard

CoaXPress

Interface standard(s)

CoaXPress 1.0, 1.1, 1.1.1, 2.0, 2.1, CoaXPress-over-Fiber Bridge Protocol 1.1

Maximum link speed

CXP-12

Maximum link width

4 connections

Camera powering

PoCXP

Connectors

Four micro-BNC 75 Ohms (also known as HD-BNC™) CXP-12

Status LEDs

One CoaXPress Host connection status LED per connection

Number of cameras

Area-scan cameras:

One 1- or 2- or 4-connection camera

Four 1-connection cameras (Contact us)

Line-scan cameras:

One 1- or 2- or 4-connection camera

Maximum number of cameras

4

Line-scan cameras supported

Yes

Maximum aggregated camera data transfer rate

50 Gbps (5,000 MB/s)

Supported CXP down-connection speeds

1.25 Gbps (CXP-1), 2.5 Gbps (CXP-2), 3.125 Gbps (CXP-3), 5 Gbps (CXP-5), 6.25 Gbps (CXP-6), 10.0 Gbps (CXP-10), and 12.5 Gbps (CXP-12)

Supported CXP up-connection speeds

Low-speed 20.83... Mbps (CXP-1 to CXP-6)

Low-speed 41.66... Mbps (CXP-10, CXP-12)

Number of CXP data streams (per camera)

1 data stream per camera

Maximum CXP stream packet size

16,384 bytes

PoCXP (Power over CoaXPress)

PoCXP Safe Power:

- PoCXP Device detection and automatic power-on

- Overload and short-circuit protections

The +24V power source (POWER) must be able to deliver the power required by the converter and camera(s)

Frame grabber / video outputs

Interface standard

CoaXPress-over-Fiber

Interface standard(s)

CXP-over-Fiber Bridge Protocol 1.1

Maximum link speed

CoF-10

Maximum link width

4 connections

Maximum data rate

50 Gbps (5,000 MB/s)

Connectors

One Enhanced Quad Small Form-factor Pluggable port (QSFP+)

Status LEDs

One CoaXPress-over-Fiber Device status LED per connection

Electrical

Supply voltage

24 V DC +/- 3% (23.25 V - 24.25 V), via external power supply (not included)

Power connector

'POWER' connector on rear side:

2-pin 5.08 mm pluggable terminal block socket

Power consumption

19.5 W for internal use

4 x 17 W for PoCXP

Environmental conditions

Operating ambient air temperature

0 °C to +50 °C / +32 °F to +122 °F

Operating ambient air humidity

10% to 90% RH non-condensing

Storage ambient air temperature

-20 °C to +70 °C / -4 °F to +158 °F

Storage ambient air humidity

10% to 90% RH non-condensing

Certifications

EMC standards

European Council EMC Directive 2014/30/EU

United States FCC rule 47 CFR 15

EMC - Emission

CISPR 32:2015 + A1:2019 Class A
EN 55032:2015 + A11:2020 Class A

EMC - Immunity

CISPR 35:2016
EN 55035:2017 + A11:2020
IEC 61000-6-2:2016 / EN 61000-6-2:2019
EN 61000-4-2
EN 61000-4-3
EN 61000-4-4
EN 61000-4-6

KC Certification

Korean Radio Waves Act, Article 58-2, Clause 3

Flammability

PCB compliant with UL 94 V-0

RoHS

European Union Directive 2015/863 (ROHS3)

REACH

European Union Regulation 1907/2006

WEEE

Must be disposed of separately from normal household waste and must be recycled according to local regulations

Ordering Information

Product code - Description

PC3629 Coaxlink CXP-12 to QSFP+ Converter

Related products

PC1676 AC-DC Power Adapter, 24V, 160W

Offices

- Europe, Middle East & Africa
Euresys SA
Contact support : support.europe@euresys.com

Sensor to Image GmbH
Contact support : support.europe@euresys.com
- China
Euresys Shanghai Liaison Office
Contact support : support.china@euresys.com

Euresys Shenzhen Liaison Office
Contact support : support.china@euresys.com
- Japan
Euresys Japan K.K.
Contact support : support.japan@euresys.com
- South Korea
Euresys South Korea Liaison Office
Contact support : support.korea@euresys.com
- Asia (other countries)
Euresys Pte. Ltd.
Contact support : support.asia@euresys.com
- North, Central & South America
Euresys Inc.
Contact support : support.usa@euresys.com

TKH Vision Experience Center
Contact support : support.usa@euresys.com